

REVALUATION RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (INDUSTRIAL AUTOMATION & RADIO FREQUENCY ENGINEERING) SEMESTER - II EXAMINATION HELD IN MAY 2015 Course : Revised Course - 2013

at No: 701	PRNo: 200907906	Sex	: M	Nam	e:	DESAI SACHIN SANTOSH
Of Attempts: 1			No Of Credits	Grad		SGPA
Embedded Syste	em & Parallel Processing					
		Theory	4	FF	F	
		IA	2	BC	P	
Industrial Data N	letworks			200	120	
		Theory	4	BB	Р	
es 120 a		IA	2	AB	P	
Microwave Engir	neering		120	-	-	
		Theory	4	CC	P	
		IA	2	AB	P	
Microwave Elect	ronics and Semiconduct				-	
		Theory	4	BB	P	
Suppose of the state of the sta	ero con con con	IA	2	BB	P	
Industrial Manag	gement	The		00	-	
		Theory	4	BC	P	
		IA	2	AA	P	
Microwave Lab				200	-	
		IA	2	BB	P	
21.1 01.002 1		Practical	2	BB	P	
Embedded Syste	em & Parallel Processing				-	
		IA	2	AB	P	
		Practical	2	BB	Р	
						6 16 5
		Total:	38			6.16 F
		NO CHAN	IGE			FAILS
eat No : 702	PRNo: 200908146	NO CHAN		Nan	ne:	
eat No: 702 o Of Attempts: 1	P R No : 200908146	NO CHAN	IGE c: M No Of	Gra	de	FAILS DESAI SIDDESH RAMAKANT
Of Attempts: 1		NO CHAN	IGE c: M		de	FAILS
Of Attempts: 1	PR No: 200908146 em & Parallel Processing	NO CHAN	No Of Credits	Gra Obta	de ined	FAILS DESAI SIDDESH RAMAKANT
Of Attempts: 1		NO CHAN Sex	No Of Credits	Gra Obta BB	de ined P	FAILS DESAI SIDDESH RAMAKANT
Of Attempts: 1 Embedded Syst	em & Parallel Processing	NO CHAN	No Of Credits	Gra Obta	de ined	FAILS DESAI SIDDESH RAMAKANT
Of Attempts: 1	em & Parallel Processing	NO CHAN Sex Theory IA	No Of Credits	Gra Obta BB BB	de ined P P	FAILS DESAI SIDDESH RAMAKANT
Of Attempts: 1 Embedded Syst	em & Parallel Processing	NO CHAN Sex Theory IA	No Of Credits	Gra Obta BB BB	de ined P P	FAILS DESAI SIDDESH RAMAKANT
Of Attempts: 1 Embedded Syst Industrial Data N	em & Parallel Processing	NO CHAN Sex Theory IA	No Of Credits	Gra Obta BB BB	de ined P P	FAILS DESAI SIDDESH RAMAKANT
Of Attempts: 1 Embedded Syst	em & Parallel Processing	Theory IA Theory IA	No Of Credits 4 2 4 2	Gra Obta BB BB BB	de ined P P	FAILS DESAI SIDDESH RAMAKANT
Of Attempts: 1 Embedded Syst Industrial Data N	em & Parallel Processing	Theory IA Theory IA Theory	No Of Credits 4 2 4 2	Gra Obta BB BB BB AA	de ined	FAILS DESAI SIDDESH RAMAKANT
Of Attempts: 1 Embedded Syst Industrial Data N Microwave Engi	em & Parallel Processing Networks Ineering	Theory IA Theory IA	No Of Credits 4 2 4 2	Gra Obta BB BB BB	de ined P P	FAILS DESAI SIDDESH RAMAKANT
Of Attempts: 1 Embedded Syst Industrial Data N Microwave Engi	em & Parallel Processing	Theory IA Theory IA Theory IA Theory IA Tor Devices	No Of Credits 4 2 4 2	Gra Obta BB BB AA AB AA	de ined P P P	FAILS DESAI SIDDESH RAMAKANT
Of Attempts: 1 Embedded Syst Industrial Data N Microwave Engi	em & Parallel Processing Networks Ineering	Theory IA Theory IA Theory IA Theory IA Theory IA Tor Devices Theory	No Of Credits 4 2 4 2 4 4	Gra Obta BB BB BB AA AB AA	de ined PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	FAILS DESAI SIDDESH RAMAKANT
Of Attempts: 1 Embedded Syst Industrial Data Microwave Engi	em & Parallel Processing Networks Ineering tronics and Semiconduc	Theory IA Theory IA Theory IA Theory IA Tor Devices	No Of Credits 4 2 4 2	Gra Obta BB BB AA AB AA	de ined P P P	FAILS DESAI SIDDESH RAMAKANT
Of Attempts: 1 Embedded Syst Industrial Data N Microwave Engi	em & Parallel Processing Networks Ineering tronics and Semiconduc	Theory IA	No Of Credits 4 2 4 2 4 2	BB BB BB AA AB AA AB AA	de ined	FAILS DESAI SIDDESH RAMAKANT
Of Attempts: 1 Embedded Syst Industrial Data Microwave Engi	em & Parallel Processing Networks Ineering tronics and Semiconduc	Theory IA Theory	No Of Credits 4 2 4 2 4 2	BB BB AA AB AA BC	de ined	FAILS DESAI SIDDESH RAMAKANT
Of Attempts: 1 Embedded Syst Industrial Data M Microwave Engi Microwave Elect Industrial Manage	em & Parallel Processing Networks Ineering tronics and Semiconduc	Theory IA	No Of Credits 4 2 4 2 4 2	BB BB BB AA AB AA AB AA	de ined	FAILS DESAI SIDDESH RAMAKANT
Of Attempts: 1 Embedded Syst Industrial Data Microwave Engi	em & Parallel Processing Networks Ineering tronics and Semiconduc	Theory IA	No Of Credits 4 2 4 2 4 2 4 2	BB BB BB AA AB AA AB AA BC AA	de ined PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	FAILS DESAI SIDDESH RAMAKANT
Of Attempts: 1 Embedded Syst Industrial Data M Microwave Engi Microwave Elect Industrial Manage	em & Parallel Processing Networks Ineering tronics and Semiconduc	Theory IA	No Of Credits 4 2 4 2 4 2 4 2 4 2	BB BB AA AB AA AB AA AB	de ined PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	FAILS DESAI SIDDESH RAMAKANT
Embedded Syst Industrial Data N Microwave Engi Microwave Elec Industrial Manag Microwave Lab	em & Parallel Processing Networks Ineering tronics and Semiconduc	Theory IA	No Of Credits 4 2 4 2 4 2 4 2	BB BB BB AA AB AA AB AA BC AA	de ined PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	FAILS DESAI SIDDESH RAMAKANT
Embedded Syst Industrial Data N Microwave Engi Microwave Elec Industrial Manag Microwave Lab	em & Parallel Processing Networks Ineering tronics and Semiconduc	Theory IA	No Of Credits 4 2 4 2 4 2 4 2 4 2 4 2	BB BB AA AB AA AB AA AB CC	de ined PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	FAILS DESAI SIDDESH RAMAKANT
Embedded Syst Industrial Data N Microwave Engi Microwave Elec Industrial Manag Microwave Lab	em & Parallel Processing Networks Ineering tronics and Semiconduc	Theory IA	No Of Credits 4 2 4 2 4 2 4 2 4 2	BB BB AA AB AA AB AA AB	de ined PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	FAILS DESAI SIDDESH RAMAKANT



NO CHANGE

PASSES



REVALUATION RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (INDUSTRIAL AUTOMATION & RADIO FREQUENCY ENGINEERING) SEMESTER - II EXAMINATION HELD IN MAY 2015 Course : Revised Course - 20 Course: Revised Course - 2013

Seat No : 704 PR No : 200	907058 Sex	c: M	Nam	e:	GAD MITESH DEVENDRA	
No Of Attempts: 1		No Of Credits	Grac Obtai		SGPA	
Embedded System & Parallel F	Processing					
	Theory	4	FF	F		
	IA	2	CC	P		
Industrial Data Networks						
	Theory	4	BC	P		
	IA	2	AB	P		
Microwave Engineering						
20	Theory	4	BC	P		
	IA	2	AA	P		
Microwave Electronics and Se	miconductor Devices					
	Theory	4	BB	P		
	IA	2	AB	P		
Industrial Management						
Charles and the San	Theory	4	CC	P		
	IA	2	AA	P		
Microwave Lab						
	IA	2	AB	P		
	Practical	2	AB	P		
Embedded System & Parallel	Processing Lab					
 ——hybraristrativatustustavatavam ▼ printuminikust2 Pičel 094 SSSR, 038/21 	IA	2	BB	P		
	Practical	2	BB	P		
	Total:	38			6.16 F Result Reserve	
	NO CHAI	NGE			Sem I Not Passed	



REVALUATION RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (INDUSTRIAL AUTOMATION & RADIO FREQUENCY ENGINEERING) SEMESTER - II EXAMINATION HELD IN MAY 2015 Course : Revised Course - 2013

COLLEGE: GOA COLLEGE OF ENGINEERING

Seat No: 707	PRNo: 201406609	Sex: F		Name: NAVGHANE SONALI VIJAY				
No Of Attempts: 1			No Of Credits	Gra Obta		SGPA		
Embedded S	ystem & Parallel Processin	g						
		Theory	4	BC	P			
		IA	2	BB	P			
Industrial Da	ta Networks							
		Theory	4	BB	P			
		IA	2	AA	P			
Microwave E	ngineering							
		Theory	4	BB	P			
		IA	2	AA	P			
Microwave E	lectronics and Semiconduc	tor Devices						
		Theory	4	AB	P			
		IA	2	AO	P			
Industrial Ma	nagement							
		Theory	4	BC	P			
		IA	2	AA	P			
Microwave L	ab							
		IA	2	AB	P			
		Practical	2	AB	P			
Embedded S	ystem & Parallel Processing	g Lab						
		IA	2	BB	Р			
		Practical	2	AA	P			
		Total:	38			7.58 P		
		NO CHAN	IGE			PASSES		

Grade	Grade Points	Performance
AO	10	Outstanding
AA	9	Excellent
AB	8	Very Good
ВВ	7	Good
ВС	6	Fair
CC	5	Satisfactory
FF	0	Fail

Read By: Dohule

Checked By:

Date: 9/10/15

S.S.J. Figueiredo
Assistant Registrar-E(Proff.)

Leo V. Macedo Controller Of Examinations

Prof. V.P. Kamat Registrar

