

COLLEGE:	GOA COLLEGE OF	<b>ENGINEERING</b>
----------	----------------	--------------------

Seat No: 3101 PR No: 201209938	Se	x : M	Nan	ne:	AMBALLOOR RO	SHUN GEORG	3E
No Of Attempts: 1		No Of Credits	Gra Obta		SGPA		
Solid State Devices and Semiconductor	Physics		7				
	Theory	4	BC	P			
	IA	2	BB	P			
Digital VLSI Design							
	Theory	4	BC	P			
	IA	2	BC	P			
Analog VLSI Design							
	Theory	4	CC	P			
	IA	2	BC	P			
VLSI Technology			*				
	Theory	4	BB	P			
	IA	2	BB	P			
Advanced Engineering Mathematics							
	Theory	4	FF	F			
	IA	2	CC	P			
HDL - LAB							
	IA	2	AA	P			
	Practical	2	AB	P			
VLSI CAD - LAB							
	IA	2	BB	P			
	Practical	2	BB	P			
	Total:	38	***************************************	200320000000	5.79 F		***************************************
5					FAILS		
Seat No: 3102 P R No: 201203266	Sex	(: F	Nam	ne :	DIVSEKAR PRAV	ISHA DINESH	
No Of Attempts: 1		No Of	Gra	de			
		Credits	Obtai		SGPA		
Solid State Devices and Semiconductor							
	Theory	4	BB	P			
	IA	2	AB	P .			
Digital VLSI Design						,	
	Theory	4	AB	P			
	IA	2	AB	P			
Analog VLSI Design		2		Р			
Analog VLSI Design	Theory	2		P			
Analog VLSI Design			AB				
Analog VLSI Design  VLSI Technology	Theory	4	AB AB	Р			
	Theory IA	4	AB AB	Р			
	Theory	4 2	AB AB AB	P P			
	Theory IA Theory	4 2 4	AB AB AB	P P			
VLSI Technology	Theory IA Theory	4 2 4	AB AB AB	P P			
VLSI Technology	Theory IA Theory IA	4 2 4 2	AB AB AB AA	P P P			
VLSI Technology	Theory IA Theory IA	4 2 4 2	AB AB AB AB AB AC	P P P			
VLSI Technology  Advanced Engineering Mathematics	Theory IA Theory IA	4 2 4 2 4 2	AB AB AB AB AB AC BC BC	P P P P			
VLSI Technology  Advanced Engineering Mathematics	Theory IA Theory IA Theory IA	4 2 4 2 4 2	AB AB AB AB AB AA BC BC AB	P P P P P			
VLSI Technology  Advanced Engineering Mathematics  HDL - LAB	Theory IA Theory IA Theory IA	4 2 4 2 4 2	AB AB AB AB AB AC BC BC	P P P P			
VLSI Technology  Advanced Engineering Mathematics	Theory IA Theory IA Theory IA IA Practical	4 2 4 2 4 2 2	AB AB AB AB AC BC BC AB BB	P P P P P			
VLSI Technology  Advanced Engineering Mathematics  HDL - LAB	Theory IA Theory IA Theory IA IA Practical	4 2 4 2 4 2 2 2	AB AB AB AB AA BC BC AB BB	P P P P P P			
VLSI Technology  Advanced Engineering Mathematics  HDL - LAB	Theory IA Theory IA Theory IA IA Practical	4 2 4 2 4 2 2	AB AB AB AB AC BC BC AB BB	P P P P P	7.63 P		

COLLEGE: GOA COLLEGE OF ENGINEERING

	o: 3103 PRNo: 201307072	Se	x: F	Nan	ne: JO	SHI SHRUTI A	ASHOK			
No Of	Attempts: 1		No Of	Gra		SGPA				
	Solid State Devices and Semiconductor	Physics	Credits	Obta	inea	001 A				
		Theory	4	AB	Р					
		IA	2	AA	P					
	Digital VLSI Design			, , ,						
	3	Theory	4	AB	Р					
		IA	2	AA	P					
	Analog VLSI Design									
		Theory	4	AB	P					
		IA	2	AO	Р					
	VLSI Technology									
		Theory	4	AA	Р					
		IA	2	ВВ	Р					
	Advanced Engineering Mathematics									
		Theory	4	ВВ	Р					
		IA	2	ВВ	P					
	HDL - LAB									
	100	IA	2	AO	Р					
		Practical	2	AA	P					
	VLSI CAD - LAB									
		IA	2	AA	Р					
		Practical	2	AA	Р					
		Total:	38	***************************************	***************************************	8.37 P				
17000100000000000000000000000000000000		000 1000 000 000 000 000 000 000 000 00			**************************************	8.37 P PASSES				
	o: 3104 PRNo: 201308908	000 1000 000 000 000 000 000 000 000 00	38 <: F	Nam	ne: KH	8.37 P PASSES AN FARHEEN	BANU	***************************************	***************************************	
	o: 3104 PRNo: 201308908 Attempts: 1	000 1000 000 000 000 000 000 000 000 00		Nam Grad		PASSES AN FARHEEN	BANU			
	Attempts: 1	Se)	: F		de	PASSES	BANU			
		Sex Physics	No Of Credits	Grad Obtai	de ned	PASSES AN FARHEEN	BANU			
	Attempts: 1	Sex Physics Theory	No Of Credits	Grad Obtai	de ned P	PASSES AN FARHEEN	BANU	***************************************		
	Attempts: 1 Solid State Devices and Semiconductor	Sex Physics	No Of Credits	Grad Obtai	de ned	PASSES AN FARHEEN	BANU			and the second
	Attempts: 1	Sex Physics Theory IA	No Of Credits	Grad Obtain AB BC	de ned P	PASSES AN FARHEEN	BANU			POARSE
	Attempts: 1 Solid State Devices and Semiconductor	Sex Physics Theory IA Theory	No Of Credits 4 2	Grad Obtain AB BC	de ned P P	PASSES AN FARHEEN	BANU			
	Attempts: 1 Solid State Devices and Semiconductor . Digital VLSI Design	Sex Physics Theory IA	No Of Credits	Grad Obtain AB BC	de ned P	PASSES AN FARHEEN	BANU			Name of the latest and the latest an
	Attempts: 1 Solid State Devices and Semiconductor	Physics Theory IA Theory IA	No Of Credits  4 2 4 2	Grad Obtain AB BC BC CC	de ned P P P	PASSES AN FARHEEN	BANU			
	Attempts: 1 Solid State Devices and Semiconductor . Digital VLSI Design	Physics Theory IA Theory IA Theory	No Of Credits  4 2 4 2	Grad Obtain AB BC BC CC	de ned P P P	PASSES AN FARHEEN	BANU			
	Attempts: 1  Solid State Devices and Semiconductor  Digital VLSI Design  Analog VLSI Design	Physics Theory IA Theory IA	No Of Credits  4 2 4 2	Grad Obtain AB BC BC CC	de ned P P P	PASSES AN FARHEEN	BANU			
	Attempts: 1 Solid State Devices and Semiconductor . Digital VLSI Design	Physics Theory IA Theory IA Theory IA	No Of Credits  4 2 4 2	Gran Obtain AB BC BC CC CC	de ned PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	PASSES AN FARHEEN	BANU			
	Attempts: 1  Solid State Devices and Semiconductor  Digital VLSI Design  Analog VLSI Design	Physics Theory IA Theory IA Theory IA Theory	No Of Credits  4 2 4 2 4 4	Gran Obtain AB BC CC CC AB	de ned PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	PASSES AN FARHEEN	BANU			
	Attempts: 1  Solid State Devices and Semiconductor  Digital VLSI Design  Analog VLSI Design  VLSI Technology	Physics Theory IA Theory IA Theory IA	No Of Credits  4 2 4 2	Gran Obtain AB BC BC CC CC	de ned PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	PASSES AN FARHEEN	BANU			
	Attempts: 1  Solid State Devices and Semiconductor  Digital VLSI Design  Analog VLSI Design	Physics Theory IA Theory IA Theory IA Theory IA	No Of Credits  4 2 4 2 4 2	Gran Obtain AB BC CC CC AB AB	de ned PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	PASSES AN FARHEEN	BANU			
	Attempts: 1  Solid State Devices and Semiconductor  Digital VLSI Design  Analog VLSI Design  VLSI Technology	Physics Theory IA Theory IA Theory IA Theory IA Theory IA Theory	No Of Credits  4 2 4 2 4 2 4 2	Gran Obtain AB BC CC CC AB AB AB	de ned PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	PASSES AN FARHEEN	BANU			
	Attempts: 1  Solid State Devices and Semiconductor  Digital VLSI Design  Analog VLSI Design  VLSI Technology  Advanced Engineering Mathematics	Physics Theory IA Theory IA Theory IA Theory IA	No Of Credits  4 2 4 2 4 2	Gran Obtain AB BC CC CC AB AB	de ned PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	PASSES AN FARHEEN	BANU			
	Attempts: 1  Solid State Devices and Semiconductor  Digital VLSI Design  Analog VLSI Design  VLSI Technology	Physics Theory IA Theory IA Theory IA Theory IA Theory IA	No Of Credits  4 2 4 2 4 2 4 2	Gran Obtain AB BC CC CC AB AB AB BB CC	de ned PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	PASSES AN FARHEEN	BANU			
	Attempts: 1  Solid State Devices and Semiconductor  Digital VLSI Design  Analog VLSI Design  VLSI Technology  Advanced Engineering Mathematics	Physics Theory IA Theory IA Theory IA Theory IA Theory IA Theory IA	No Of Credits  4 2 4 2 4 2 4 2	Gran Obtain AB BC CC CC AB AB AB BB CC BBB	de ned PP PP PP PP PP PP	PASSES AN FARHEEN	BANU			
	Attempts: 1  Solid State Devices and Semiconductor  Digital VLSI Design  Analog VLSI Design  VLSI Technology  Advanced Engineering Mathematics  HDL - LAB	Physics Theory IA Theory IA Theory IA Theory IA Theory IA	No Of Credits  4 2 4 2 4 2 4 2	Gran Obtain AB BC CC CC AB AB AB BB CC	de ned PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	PASSES AN FARHEEN	BANU			
	Attempts: 1  Solid State Devices and Semiconductor  Digital VLSI Design  Analog VLSI Design  VLSI Technology  Advanced Engineering Mathematics	Physics Theory IA Theory IA Theory IA Theory IA Theory IA IA Theory IA	No Of Credits  4 2 4 2 4 2 4 2 4 2	Gran Obtain  AB BC CC CC AB AB AB BB CC BB BB BC CC	de ned PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	PASSES AN FARHEEN	BANU			
	Attempts: 1  Solid State Devices and Semiconductor  Digital VLSI Design  Analog VLSI Design  VLSI Technology  Advanced Engineering Mathematics  HDL - LAB	Physics Theory IA Theory IA Theory IA Theory IA Theory IA IA Theory IA	No Of Credits  4 2 4 2 4 2 4 2 4 2 4 2	Gran Obtain  AB BC CC CC AB AB AB BB CC BB BB	de ned PP PP PP PP PP PP PP	PASSES AN FARHEEN	BANU			
	Attempts: 1  Solid State Devices and Semiconductor  Digital VLSI Design  Analog VLSI Design  VLSI Technology  Advanced Engineering Mathematics  HDL - LAB	Physics Theory IA Theory IA Theory IA Theory IA Theory IA IA Theory IA	No Of Credits  4 2 4 2 4 2 4 2 4 2	Gran Obtain  AB BC CC CC AB AB AB BB CC BB BB BC CC	de ned PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	PASSES AN FARHEEN	BANU			



COLLECE	COACOLI	FOF OF	FNGINFFRING
COLLEGE	(IUA (JUI)	F(3F()F	FIVE-INFERING

eat No : 3105 PR No : 201203287	Se	x: F			NAIK RUTHVI UDAY	
o Of Attempts: 1		No Of Credits	Gra Obta		SGPA	
Solid State Devices and Semiconductor	Physics					
	Theory	4	AB	P		
	IA	.2	BC	P		
Digital VLSI Design						
	Theory	4	BC	P		
	IA	2	BB	P		
Analog VLSI Design	T1		-	_		
	Theory	4	BB	Р		
VI SI Tashnalagu	IA	2	AB	Р		
VLSI Technology	Theory	4	ВС	Р		
	IA	4	AB	P		
Advanced Engineering Mathematics	IA	2	Ab			
Advanced Engineering Mathematics	Theory	4	CC	Р		
	IA	2	CC	Р		
HDL - LAB		~	- 00			
,	IA	2	AB	Р	, ,	
	Practical	2	ВВ	P		
VLSI CAD - LAB		-				
	IA	2	AB	Р		
	Practical	2	AB	P		
	Total:	38	A STATE OF THE PARTY OF THE PAR	diversor	6.79 P	
No 2400 D.D.No 204202704			N I	MET POSE VOCA POCA POCA POCA	PASSES	
at No : 3106 P R No : 201203721	Sex	k: F	Ivan	ie:	SHET SHRUTI VINAYAK	
Of Attempts: 1			_			
Of Attempts: 1		No Of Credits	Gra		SGPA	
Of Attempts: 1  Solid State Devices and Semiconductor	Physics	No Of Credits	Grad Obtai		SGPA	
	Physics Theory				SGPA	
		Credits	Obtai	ined	SGPA	
	Theory	Credits 4	Obtai	ined P	SGPA	
Solid State Devices and Semiconductor	Theory IA Theory	Credits 4 2	Obtai BB BB	P P	SGPA	
Solid State Devices and Semiconductor  Digital VLSI Design	Theory	Credits 4 2	Obtai BB BB	P P	SGPA	
Solid State Devices and Semiconductor	Theory IA Theory IA	Credits  4 2 4 2	BB BB AB BB	P P P	SGPA	
Solid State Devices and Semiconductor  Digital VLSI Design	Theory IA Theory IA Theory	Credits  4 2 4 2	Obtai BB BB AB BB	P P P	SGPA	
Solid State Devices and Semiconductor  Digital VLSI Design  Analog VLSI Design	Theory IA Theory IA	Credits  4 2 4 2	BB BB AB BB	P P P	SGPA	
Solid State Devices and Semiconductor  Digital VLSI Design	Theory IA Theory IA Theory IA	Credits  4 2 4 2	BB BB AB BB AB	P P P P	SGPA	
Solid State Devices and Semiconductor  Digital VLSI Design  Analog VLSI Design	Theory IA Theory IA Theory IA Theory	4 2 4 2 4 4	BB BB BB BB AB BC	P P P P P	SGPA	
Solid State Devices and Semiconductor  Digital VLSI Design  Analog VLSI Design  VLSI Technology	Theory IA Theory IA Theory IA	Credits  4 2 4 2	BB BB AB BB AB	P P P P	SGPA	
Solid State Devices and Semiconductor  Digital VLSI Design  Analog VLSI Design	Theory IA Theory IA Theory IA Theory IA	4 2 4 2 4 2 4 2	BB BB BB BB AB BC BB	P P P P P	SGPA	
Solid State Devices and Semiconductor  Digital VLSI Design  Analog VLSI Design  VLSI Technology	Theory IA Theory IA Theory IA Theory IA Theory IA	4 2 4 2 4 2 4 4 2	BB BB BB BB AB BC BB CC	PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	SGPA	
Solid State Devices and Semiconductor  Digital VLSI Design  Analog VLSI Design  VLSI Technology  Advanced Engineering Mathematics	Theory IA Theory IA Theory IA Theory IA	4 2 4 2 4 2 4 2	BB BB BB BB AB BC BB	P P P P P	SGPA	
Solid State Devices and Semiconductor  Digital VLSI Design  Analog VLSI Design  VLSI Technology	Theory IA Theory IA Theory IA Theory IA Theory IA	4 2 4 2 4 2 4 2	BB BB BB AB BC BB	ned PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	SGPA	
Solid State Devices and Semiconductor  Digital VLSI Design  Analog VLSI Design  VLSI Technology  Advanced Engineering Mathematics	Theory IA Theory IA Theory IA Theory IA Theory IA Theory IA	4 2 4 2 4 2 2 2 2	BB BB BB AB BC BB AA	ned PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	SGPA	
Solid State Devices and Semiconductor  Digital VLSI Design  Analog VLSI Design  VLSI Technology  Advanced Engineering Mathematics  HDL - LAB	Theory IA Theory IA Theory IA Theory IA Theory IA	4 2 4 2 4 2 4 2	BB BB BB AB BC BB	ned PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	SGPA	
Digital VLSI Design  Analog VLSI Design  VLSI Technology  Advanced Engineering Mathematics	Theory IA	4 2 4 2 4 2 2 2 2 2	BB BB BB AB BC BB AA AB	ned PP PP PP PP PP	SGPA	
Solid State Devices and Semiconductor  Digital VLSI Design  Analog VLSI Design  VLSI Technology  Advanced Engineering Mathematics  HDL - LAB	Theory IA Theory IA Theory IA Theory IA Theory IA Theory IA IA Practical	4 2 4 2 4 2 2 2 2 2 2	BB BB BB AB BC BB AA AB AB	ned PP	SGPA	
Solid State Devices and Semiconductor  Digital VLSI Design  Analog VLSI Design  VLSI Technology  Advanced Engineering Mathematics  HDL - LAB	Theory IA	4 2 4 2 4 2 2 2 2 2	BB BB BB AB BC BB AA AB	ned PP PP PP PP PP	SGPA	



COLLEGE: GOA COLLEGE OF ENGINEERING

Seat No: 3107 PR No: 201611666	Se	x: F	Nam	ne :	RASHINKAR PRAG	ATI GANAPATHI
No Of Attempts: 3		No Of Credits	Gra Obta		SGPA	
Solid State Devices and Semiconductor	r Physics					
	Theory	4	CC	+		
	IA	2	BC	+		
Digital VLSI Design						
	Theory	4	CC	+		
	IA	2	BC	+		
Analog VLSI Design						
	Theory	4	BB	P		
	IA	2	CC	+		
VLSI Technology						
· · · · · · · · · · · · · · · · · · ·	Theory	4	CC	+		
	IA	2	BB	+		
Advanced Engineering Mathematics						
	Theory	4	CC	+		
	IA	2	CC	+		
HDL - LAB						
	IA	2	AB	+		*
	Practical	2	BB	+		
VLSI CAD - LAB						
	IA	2	AB	+		
	Practical	2	ВВ	+		
	Total:	38	***************************************	*********	5.95 P PASSES	6 S S

Grade	Grade Points	Performance
AO	10	Outstanding
AA	9	Excellent
AB	. 8	Very Good
ВВ	7	Good
ВС	6	Fair
CC	5	Satisfactory
FF	0	Fail

Read By: photo

Checked By:

Date: 14/2/18

Vivek Salaskar Assistant Registrar-E(Proff.) Prof. Antiradha Wagle Controller Of Examinations Prof. Y. V. Reddy Registrar

