GOA UNIVERSITY
Taleigao Plateau, Goa 403 206

RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - I EXAMINATION HELD IN NOVEMBER 2017

Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3101   P R No : 201209938   Sex : M   Name : AMBALLOOR ROSHUN GEORGE

No Of Attempts : 1

		No Of Credits	Grade Obtained		SGPA
Solid State Devices and Semiconductor Physics					
	Theory	4	BC	P	
	IA	2	BB	P	
Digital VLSI Design					
	Theory	4	BC	P	
	IA	2	BC	P	
Analog VLSI Design					
	Theory	4	CC	P	
	IA	2	BC	P	
VLSI Technology					
	Theory	4	BB	P	
	IA	2	BB	P	
Advanced Engineering Mathematics					
	Theory	4	FF	F	
	IA	2	CC	P	
HDL - LAB					
	IA	2	AA	P	
	Practical	2	AB	P	
VLSI CAD - LAB					
	IA	2	BB	P	
	Practical	2	BB	P	
	Total :	38			5.79 F
					FAILS

Seat No : 3102   P R No : 201203266   Sex : F   Name : DIVSEKAR PRAVISHA DINESH

No Of Attempts : 1

		No Of Credits	Grade Obtained		SGPA
Solid State Devices and Semiconductor Physics					
	Theory	4	BB	P	
	IA	2	AB	P	
Digital VLSI Design					
	Theory	4	AB	P	
	IA	2	AB	P	
Analog VLSI Design					
	Theory	4	AB	P	
	IA	2	AB	P	
VLSI Technology					
	Theory	4	AB	P	
	IA	2	AA	P	
Advanced Engineering Mathematics					
	Theory	4	BC	P	
	IA	2	BC	P	
HDL - LAB					
	IA	2	AB	P	
	Practical	2	BB	P	
VLSI CAD - LAB					
	IA	2	AA	P	
	Practical	2	AB	P	
	Total :	38			7.63 P
					PASSES

P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; X/NE: Not Eligible; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cummulative Grade Point Average

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HELD IN NOVEMBER 2017   Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3103   P R No : 201307072   Sex : F   Name : JOSHI SHRUTI ASHOK
No Of Attempts : 1

		No Of Credits	Grade Obtained		SGPA
Solid State Devices and Semiconductor Physics					
	Theory	4	AB	P	
	IA	2	AA	P	
Digital VLSI Design					
	Theory	4	AB	P	
	IA	2	AA	P	
Analog VLSI Design					
	Theory	4	AB	P	
	IA	2	AO	P	
VLSI Technology					
	Theory	4	AA	P	
	IA	2	BB	P	
Advanced Engineering Mathematics					
	Theory	4	BB	P	
	IA	2	BB	P	
HDL - LAB					
	IA	2	AO	P	
	Practical	2	AA	P	
VLSI CAD - LAB					
	IA	2	AA	P	
	Practical	2	AA	P	
	Total :	38			8.37 P
					PASSES

Seat No : 3104   P R No : 201308908   Sex : F   Name : KHAN FARHEEN BANU
No Of Attempts : 1

		No Of Credits	Grade Obtained		SGPA
Solid State Devices and Semiconductor Physics					
	Theory	4	AB	P	
	IA	2	BC	P	
Digital VLSI Design					
	Theory	4	BC	P	
	IA	2	CC	P	
Analog VLSI Design					
	Theory	4	CC	P	
	IA	2	AB	P	
VLSI Technology					
	Theory	4	AB	P	
	IA	2	AB	P	
Advanced Engineering Mathematics					
	Theory	4	BB	P	
	IA	2	CC	P	
HDL - LAB					
	IA	2	BB	P	
	Practical	2	BC	P	
VLSI CAD - LAB					
	IA	2	BB	P	
	Practical	2	BB	P	
	Total :	38			6.68 P
					PASSES

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Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3105   P R No : 201203287   Sex : F   Name : NAIK RUTHVI UDAY

No Of Attempts : 1

		No Of Credits	Grade Obtained		SGPA
Solid State Devices and Semiconductor Physics					
	Theory	4	AB	P	
	IA	2	BC	P	
Digital VLSI Design					
	Theory	4	BC	P	
	IA	2	BB	P	
Analog VLSI Design					
	Theory	4	BB	P	
	IA	2	AB	P	
VLSI Technology					
	Theory	4	BC	P	
	IA	2	AB	P	
Advanced Engineering Mathematics					
	Theory	4	CC	P	
	IA	2	CC	P	
HDL - LAB					
	IA	2	AB	P	
	Practical	2	BB	P	
VLSI CAD - LAB					
	IA	2	AB	P	
	Practical	2	AB	P	
	Total :	38			6.79 P

PASSES

Seat No : 3106   P R No : 201203721   Sex : F   Name : SHET SHRUTI VINAYAK

No Of Attempts : 1

		No Of Credits	Grade Obtained		SGPA
Solid State Devices and Semiconductor Physics					
	Theory	4	BB	P	
	IA	2	BB	P	
Digital VLSI Design					
	Theory	4	AB	P	
	IA	2	BB	P	
Analog VLSI Design					
	Theory	4	BB	P	
	IA	2	AB	P	
VLSI Technology					
	Theory	4	BC	P	
	IA	2	BB	P	
Advanced Engineering Mathematics					
	Theory	4	CC	P	
	IA	2	BB	P	
HDL - LAB					
	IA	2	AA	P	
	Practical	2	AB	P	
VLSI CAD - LAB					
	IA	2	AB	P	
	Practical	2	AB	P	
	Total :	38			7.11 P

PASSES

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Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3107   P R No : 201611666   Sex : F   Name : RASHINKAR PRAGATI GANAPATHI

No Of Attempts : 3

		No Of Credits	Grade Obtained		SGPA
Solid State Devices and Semiconductor Physics					
	Theory	4	CC	+	
	IA	2	BC	+	
Digital VLSI Design					
	Theory	4	CC	+	
	IA	2	BC	+	
Analog VLSI Design					
	Theory	4	BB	P	
	IA	2	CC	+	
VLSI Technology					
	Theory	4	CC	+	
	IA	2	BB	+	
Advanced Engineering Mathematics					
	Theory	4	CC	+	
	IA	2	CC	+	
HDL - LAB					
	IA	2	AB	+	
	Practical	2	BB	+	
VLSI CAD - LAB					
	IA	2	AB	+	
	Practical	2	BB	+	
Total :		38			5.95 P
					PASSES

Grade	Grade Points	Performance
AO	10	Outstanding
AA	9	Excellent
AB	8	Very Good
BB	7	Good
BC	6	Fair
CC	5	Satisfactory
FF	0	Fail

Read By :

Checked By :

Date : 14/2/18

Vivek Salaskar
Assistant Registrar-E(Proff.)

Prof. Anuradha Wagle
Controller Of Examinations

Prof. Y. V. Reddy
Registrar

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