



GOA UNIVERSITY
Taleigao Plateau, Goa 403 206

RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - II EXAMINATION
HELD IN MAY 2018 Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3201 P R No : 201209938 Sex : M Name : AMBALLOOR ROSHUN GEORGE
No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA
ASIC Design & FPGA	Theory	4	BC P	
	IA	2	AA P	
Digital Signal Processors & Embedded Systems	Theory	4	BB P	
	IA	2	AB P	
Design for Testability & E-Waste Management	Theory	4	BC P	
	IA	2	AO P	
Processor Architecture & Parallel Processing	Theory	4	FF F	
	IA	2	AB P	
Memory Design	Theory	4	BB P	
	IA	2	AA P	
Parallel Processing Lab	IA	2	AB P	
	Practical	2	AB P	
FPGA & Embedded Systems Lab	IA	2	AA P	
	Practical	2	AA P	
Total:		38		6.84 F FAILS

Seat No : 3202 P R No : 201203266 Sex : F Name : DIVSEKAR PRAVISHA DINESH
No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA
ASIC Design & FPGA	Theory	4	AB P	
	IA	2	AA P	
Digital Signal Processors & Embedded Systems	Theory	4	AB P	
	IA	2	AA P	
Design for Testability & E-Waste Management	Theory	4	BC P	
	IA	2	AB P	
Processor Architecture & Parallel Processing	Theory	4	AB P	
	IA	2	AB P	
Memory Design	Theory	4	BB P	
	IA	2	AA P	
Parallel Processing Lab	IA	2	AB P	
	Practical	2	AB P	
FPGA & Embedded Systems Lab	IA	2	AA P	
	Practical	2	AB P	
Total:		38		7.89 P PASSES

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20/08/2018

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Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3203	P R No : 201307072	Sex : F	Name : JOSHI SHRUTI ASHOK
No Of Attempts : 1		No Of Credits	Grade Obtained SGPA
ASIC Design & FPGA			
	Theory	4	BB P
	IA	2	AA P
Digital Signal Processors & Embedded Systems			
	Theory	4	AB P
	IA	2	AA P
Design for Testability & E-Waste Management			
	Theory	4	AB P
	IA	2	AA P
Processor Architecture & Parallel Processing			
	Theory	4	AA P
	IA	2	AA P
Memory Design			
	Theory	4	AB P
	IA	2	AO P
Parallel Processing Lab			
	IA	2	AB P
	Practical	2	AB P
FPGA & Embedded Systems Lab			
	IA	2	AA P
	Practical	2	BB P
Total :		38	8.32 P PASSES

Seat No : 3204	P R No : 201308908	Sex : F	Name : KHAN FARHEEN BANU
No Of Attempts : 1		No Of Credits	Grade Obtained SGPA
ASIC Design & FPGA			
	Theory	4	BB P
	IA	2	BB P
Digital Signal Processors & Embedded Systems			
	Theory	4	AA P
	IA	2	BB P
Design for Testability & E-Waste Management			
	Theory	4	BC P
	IA	2	AB P
Processor Architecture & Parallel Processing			
	Theory	4	BC P
	IA	2	BB P
Memory Design			
	Theory	4	BC P
	IA	2	AB P
Parallel Processing Lab			
	IA	2	AB P
	Practical	2	BB P
FPGA & Embedded Systems Lab			
	IA	2	AA P
	Practical	2	BC P
Total :		38	7.11 P PASSES

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HELD IN MAY 2018

Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3205 P R No : 201203287 Sex : F Name : NAIK RUTHVI UDAY
No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA
ASIC Design & FPGA	Theory	4	BB P	
	IA	2	AB P	
Digital Signal Processors & Embedded Systems	Theory	4	BB P	
	IA	2	BB P	
Design for Testability & E-Waste Management	Theory	4	CC P	
	IA	2	BC P	
Processor Architecture & Parallel Processing	Theory	4	BB P	
	IA	2	AB P	
Memory Design	Theory	4	BB P	
	IA	2	BB P	
Parallel Processing Lab	IA	2	AB P	
	Practical	2	AB P	
FPGA & Embedded Systems Lab	IA	2	AA P	
	Practical	2	BB P	
Total:		38		7.05 P PASSES

Seat No : 3206 P R No : 201203721 Sex : F Name : SHET SHRUTI VINAYAK
No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA
ASIC Design & FPGA	Theory	4	BB P	
	IA	2	AB P	
Digital Signal Processors & Embedded Systems	Theory	4	AA P	
	IA	2	AA P	
Design for Testability & E-Waste Management	Theory	4	BC P	
	IA	2	AB P	
Processor Architecture & Parallel Processing	Theory	4	BB P	
	IA	2	AA P	
Memory Design	Theory	4	BB P	
	IA	2	AA P	
Parallel Processing Lab	IA	2	AB P	
	Practical	2	AB P	
FPGA & Embedded Systems Lab	IA	2	AA P	
	Practical	2	AB P	
Total:		38		7.79 P PASSES



Grade	Grade Points	Performance
AO	10	Outstanding
AA	9	Excellent
AB	8	Very Good
BB	7	Good
BC	6	Fair
CC	5	Satisfactory
FF	0	Fail

Read By : *phib*
Checked By : *KVL*
Date : 10/8/2018

H Gaonkar
Aniketh Gaonkar
Assistant Registrar-E(Prof.)

Anuradha
Prof. Anuradha Wagle
Controller Of Examinations

YV Reddy
Prof. Y. V. Reddy
Registrar

P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; X/NE: Not Eligible; +: Grades Carried Over; SGPA: Semester Grade Point Average;
CGPA: Cumulative Grade Point Average

Discrepancy, if any in the Result shall be informed to the University within a week from the date of receipt of the result by the college