GOA UNIVERSITY

ENGINEERING EXAMINATION SYSTEM

COLLEGE WISE STUDENT REVALUATION RESULT REGISTER FOR SEMESTER-VII EXAM

Bachelor of Engineering (Electrical & Electronics)

College :-Government of Goa College of Engineering

RC 2016-17

EXAM YEAR:November 2019

SEAT NO	P.R. NO.	ATTEMPTS	ARE CD	PRO SEM ESTE R-VII	SEX	NAME OF THE CANDIDATE	MAX. MIN.	THEORY 100 40	SESSIONAL 25	TERM WORK 25	TOTAL 125 OR 150 50 OR 60	PRACTICAL 50 OR 25 25 OR 10	ORAL 50 OR 25 25 OR 10	TOTAL 725 290	REMARK
190534	201610514	1	A		М	ABDAR AKS	HAY SH	HRIPATI							
	Switchgear	and Protect	ion			1		72	16		88P				
	Advanced D							83	18		101P				
	VLSI Circuit							53	17		70P	22P			
	Smart Grid	5						76	14		90P		21P		
	Fuzzy Logic	and Neural	Netwo	orks				66	19		85P		19P		_
	Project												23P		
														519	Passes
190535	201610516	1	А		М	BAKHLE AM	OGH C	HANDRA	SHEKAR						
100000	Switchgear						00110	58	14		72P				
	Advanced D							77	15		92P				
	VLSI Circuit		ontroi					41	13		54P	19P			
	Smart Grid	Design						71	10		82P		19P		
	Fuzzy Logic	and Neural	Notwo	orke				61	17		78P		18P		
	Project		INELW	JIKS				01	17		7.01		22P		
	FIOJECI													456	Passes
														400	1 03363
190536	201611239	1	A		Μ	BANDEKAR	AMEY					1			
	Switchgear							44	13		57P				
	Advanced D	rives and C	ontrol					51	15		66P				
	VLSI Circuit	Design						28	13		41F	19P			
	Smart Grid							63	11		74P		18P		
	Fuzzy Logic	and Neural	Netwo	orks				57	13		70P		19P		
	Project												23P		
														387 #7	Fails
190537	201610518	1	А		М	BHAT KAUS	HIK AM	IOL							
	Switchgear	and Protect	ion					68	17		85P				
	Advanced D							67	22		89P				
	VLSI Circuit							59	14		73P	25P			
	Smart Grid							71	13		84P		22P		
	Fuzzy Logic	and Neural	Netwo	orks				65	20		85P		23P		
	Project												24P		
	,													510	Passes
190538	201610520	1	A		F	BHAT TERE	<u>۹</u>								
190030		-			Г	DHATTERE	5A	76	20		060				
	Switchgear							76	20		96P				
	Advanced D		ontrol					85	25		110P	050			
	VLSI Circuit	Design						86	23		109P	25P	000		
	Smart Grid							80	21		101P		23P		
	Fuzzy Logic	and Neural	Netwo	orks				76	23		99P		24P		
	Project												24P	011	
														611	Passes
190539	201610526	1	А		М	DHARGALK	AR ANA	NT ALIA	S OMKAR K	IRAN					
	Switchgear	and Protect	ion					69	18		87P				
	Advanced D	rives and C	ontrol					81	20		101P				
	VLSI Circuit	Design						60	18		78P	18P			
	Smart Grid							79	14		93P		21P		
	Fuzzy Logic	and Neural	Netwo	orks				75	17		92P		22P		
	Project												23P		
	-								Į		1			535	Passes

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190540	201610528 1 A M DHOND DATTA							
	Switchgear and Protection	86	19	105P				
	Advanced Drives and Control	88	24	112P				
	VLSI Circuit Design	82	20	102P	25P			
	Smart Grid	83	20	103P		22P		
	Fuzzy Logic and Neural Networks	90	24	114P		24P		
	Project					23P		
							630	Passes

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	190547	201610542	1	A	N	/	GAONKAR OMKAR DINESH
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		4.0	74P				
Switchgear and Protection	56	18	748				
Advanced Drives and Control	82	18	100P				
VLSI Circuit Design	59	17	76P	19P			
Electrical Design Estimation and Costing	64	19	83P		16P		
Fuzzy Logic and Neural Networks	68	19	87P		21P		
Project					22P		
						498 #7	Passes

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190554 201610570 1 A M KERKAR RAHUL DAMODAR Page: 3 Of 8

Switchgear and Protection	60	12	72P				
Advanced Drives and Control	51	21	72P				
VLSI Circuit Design	56	15	71P	24P			
Electrical Design Estimation and Costing	65	13	78P		15P		
Fuzzy Logic and Neural Networks	79	22	101P		20P		
Project					22P		
						475	Passes

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Switchgear and Protection

ENGINEERING EXAMINATION SYSTEM

COLLEGE WISE STUDENT REVALUATION RESULT REGISTER FOR SEMESTER-VII EXAM

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Bachelor of Engineering (Electrical & Electronics) RC 2016-17 College :-Government of Goa College of Engineering EXAM YEAR:November 2019 SEAT NO P.R. NO. ATTEMPTS ARE PRO SEX NAME OF THE THEORY SESSIONAL TERM WORK TOTAL PRACTICAL ORAL TOTAL CANDIDATE MAX. 125 OR 150 REMARKS CD SEM 100 25 50 OR 25 50 OR 25 725 25 25 OR 10 50 OR 60 25 OR 10 290 ESTE MIN. 40 R-VII 190555 201610571 1 Α Μ **KESARKAR SAI SANDESH** 70 13 83P Switchgear and Protection 80 18 98P Advanced Drives and Control 53 13 66P 21P VLSI Circuit Design 73 14 87P 18P **Electrical Design Estimation and Costing** 74P 20P 55 19 Fuzzy Logic and Neural Networks 23P Project 490 Passes KHANDOLKAR TEJNATH UTTAM 190556 201704892 1 A Μ 74 85P Switchgear and Protection 11 63 16 79P Advanced Drives and Control 49P #1 **VLSI** Circuit Design 39#1 10 14P **Electrical Design Estimation and Costing** 61 15 76P 19P 60P 45 15 18P Fuzzy Logic and Neural Networks 20P Project 420 #7 Passes 190558 KOLAMBKAR PRAJAKTA PRAKASH 201610579 1 A F **RESULT WITHHELD-(SEM.5 NOT PASSED)** 70 85P Switchgear and Protection 15 52P 40 12 Advanced Drives and Control 50P \$5 15P 35\$5 15 VLSI Circuit Design 50 13 63P 16P **Electrical Design Estimation and Costing** 51 12 63P 15P Fuzzy Logic and Neural Networks 21P Project 190559 201704929 IF. KOTTALGI VIBHA 1 Α Switchgear and Protection 64 20 84P 69 22 Advanced Drives and Control 91P 63 21P **VLSI** Circuit Design 21 84P 76 **Electrical Design Estimation and Costing** 25 101P 20P Fuzzy Logic and Neural Networks 80 20 100P 21P Project 22P 544 Passes 190560 1 Μ KSHITIJ AJAY DESHMUKH 201610749 A Switchgear and Protection 62 15 77P 74 Advanced Drives and Control 15 89P 55 18 73P 20P VLSI Circuit Design 69 16 85P 19P **Electrical Design Estimation and Costing** 67 20 87P 20P Fuzzy Logic and Neural Networks 23P Project 493 #7 Passes 190561 LALAN KUMAR SINGH M.S. 201610770 1 Α Μ 71 14 85P Switchgear and Protection 73 15 88P Advanced Drives and Control 57 15 72P 20P VLSI Circuit Design 68 11 79P 19P Smart Grid 64 20 84P 21P Fuzzy Logic and Neural Networks 23P Project 491 Passes 190562 201610464 MALWANKER SAHIL MAHARUDRA 1 A M 68 16 84P

		0.4	107P				
Advanced Drives and Control	83	24	107P				
VLSI Circuit Design	51	15	66P	24P			
Smart Grid	67	15	82P		22P		
Fuzzy Logic and Neural Networks	70	20	90P		22P		
Project					22P		
						519	Passes

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Bachelor of Engineering (Electrical & Electronics) RC 2016-17 College :-Government of Goa College of Engineering EXAM YEAR:November 2019 SEAT NO P.R. NO. ATTEMPTS ARE PRO SEX NAME OF THE THEORY SESSIONAL TERM WORK TOTAL PRACTICAL ORAL TOTAL CANDIDATE MAX. 125 OR 150 50 OR 25 50 OR 25 REMARKS CD SEM 100 25 725 25 ESTE 50 OR 60 25 OR 10 25 OR 10 290 MIN. 40 R-VII 190563 201611242 1 Α Μ MARGAJ SHUBHAM PRAKASH 73 13 86P Switchgear and Protection 68 7 75P Advanced Drives and Control 45 10 55P 14P VLSI Circuit Design 62 11 73P 15P **Electrical Design Estimation and Costing** 62P 14P 50 12 Fuzzy Logic and Neural Networks 16P Project 410 Passes 190564 201610595 1 A Μ NAIK PRABHAV RAJENDRA 42 15 57P Switchgear and Protection 77 15 92P Advanced Drives and Control 69P 20P **VLSI** Circuit Design 50 19 Smart Grid 76 11 87P 20P 75P 19P 58 17 Fuzzy Logic and Neural Networks 23P Project 462 Passes 190565 NAIK PRAJYOT RAJENDRA 201610598 1 A Μ 67 15 82P Switchgear and Protection 82 20 102F Advanced Drives and Control 18 20P **VLSI** Circuit Design 58 76P 22 66 88P 20P **Electrical Design Estimation and Costing** 67 15 82P 19P Fuzzy Logic and Neural Networks 23P Project 512 #7 Passes A 190566 201610599 1 IF. NAIK PRIYANKA PANDURANG Switchgear and Protection 40 12 52P 49 12 61P Advanced Drives and Control 45 15 60P 19P VLSI Circuit Design 42 8 50P 13P **Electrical Design Estimation and Costing** 40 18 58P 16P Fuzzy Logic and Neural Networks 22P Project 351 Passes 190567 201705112 A Μ NAIK SAEEL PRAKASH 1 Switchgear and Protection 66 16 82P 13 80 93P Advanced Drives and Control 15 50P #5 18P VLSI Circuit Design 35#5 23 **Electrical Design Estimation and Costing** 68 91P 20P 13 74P 16P 61 Fuzzy Logic and Neural Networks 22P Project 466 #7 Passes 190568 201610602 1 A Μ NAIK SAHIL GAJANAN 64 11 75P Switchgear and Protection 15 78 93P Advanced Drives and Control 15 76P 23P **VLSI** Circuit Design 61 76 10 86P 20P Smart Grid 17 73P 56 21P Fuzzy Logic and Neural Networks 24P Project 491 Passes

		4.4	84P				
Switchgear and Protection	70	14	04P				
Advanced Drives and Control	67	15	82P				
VLSI Circuit Design	44	18	62P	18P			
Electrical Design Estimation and Costing	61	15	76P		16P		
Fuzzy Logic and Neural Networks	73	20	93P		19P		
Project					15P		
						465	Passes

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COLLEGE WISE STUDENT REVALUATION RESULT REGISTER FOR SEMESTER-VII EXAM

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P.R. NO.	ATTEMPTS		PRO SEM	SEX	NAME OF THE CANDIDATE	MAX.	THEORY 100	SESSIONAL	TERM WORK	TOTAL 125 OR 150	PRACTICAL 50 OR 25	ORAL 50 OR 25	TOTAL 725	REMARK
004040000	1		ESTE R-VII	-		MIN .	40			50 OR 60	25 OR 10	25 OR 10		
201610609		A		F	NAIK TEJAS	WINI I (58	1		70P				
Switchgear a							56 75	12 20		95P				
Advanced D		ontrol					-				24P			
	-	ation a		ting							241	16P		
	-			sung								-		
Project		Netwo	JIK5									23P		
	1				I					1			471	Passes
	1			Μ	PARAB AJIN	KYA BA		1			THHELD-(SEM.	5 NOT PASSI	ED)	
-														
		ontrol									-			
	0										18P			
	-			sting										
Fuzzy Logic	and Neural	Netwo	orks				60	13		73P		16P		
Project												23P		
201610614	1	А		М	PATIL SAUR	ABH PA	RASHAI	RAM						
Switchgear a	and Protecti	on					66	19		85P				
							85	24		109P				
							77	22		99P	25P			
Smart Grid	~						74	16		90P		21P		
	and Neural	Netwo	orks				65	21		86P		24P		
			-									24P		
													563	Passes
201610767	1	A		F	PRATIKSHA	NAGRA	J PATIL							
Switchgear a	and Protecti	on					61	11		72P				
							82	19		101P				
VLSI Circuit	Design						62	18		80P	20P			
	-	ation a	nd Cos	sting			63	19		82P		19P		
	-			5			77	16		93P		20P		
												21P		
,													508 #7	Passes
201610618	1	А		F	SAKROJI VE	DA VIN	AYAK							
Switchgear a	and Protecti	on					64	11		75P				
Advanced D	rives and C	ontrol					73	17		90P				
VLSI Circuit	Design						45	17		62P	17P			
	_	ation a	nd Cos	stina			70	13		83P		12P		
	-						52	10		62P		15P		
Project												20P		
													436 #7	Passes
		•		М	SALUNKHE	SIDDHA	NTA SH	RIKANT						
201610622	1	A						40		66P				
201610622 Switchgear a							53	13						
	and Protecti	on					53 60	13 22		82P				
Switchgear a	and Protecti rives and C	on								82P 71P	20P			
Switchgear a Advanced D	and Protecti rives and C Design	on ontrol		sting			60	22			20P	14P		
Switchgear a Advanced D VLSI Circuit	and Protecti rives and C Design sign Estima	on ontrol ation ar	nd Cos	sting			60 52	22 19		71P	20P	14P 18P		
Switchgear a Advanced D VLSI Circuit Electrical De	and Protecti rives and C Design sign Estima	on ontrol ation ar	nd Cos	sting			60 52 60	22 19 13		71P 73P	20P			
Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic	and Protecti rives and C Design sign Estima	on ontrol ation ar	nd Cos	sting			60 52 60	22 19 13		71P 73P	20P	18P	436 #7	Passes
Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic	and Protecti rives and C Design sign Estima	on ontrol ation ar	nd Cos	sting	SANDILYA P	OOJA S	60 52 60 51	22 19 13 19		71P 73P	20P	18P	436 #7	Passes
Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic Project	and Protecti rives and C Design sign Estima and Neural	on ontrol ation ar Netwo	nd Cos			OOJA S	60 52 60 51	22 19 13 19		71P 73P	20P	18P	436 #7	Passes
Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic Project	and Protecti rives and C Design sign Estima and Neural 1 and Protecti	on ontrol ation ar Netwo A	nd Cos			OOJA S	60 52 60 51 SIYARAM	22 19 13 19		71P 73P 70P	20P	18P	436 #7	Passes
Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic Project 201610628 Switchgear a Advanced D	and Protecti rives and C Design sign Estima and Neural 1 and Protecti rives and C	on ontrol ation ar Netwo A	nd Cos			OOJA S	60 52 60 51 SIYARAM 60	22 19 13 19 19 17		71P 73P 70P 77P	20P	18P	436 #7	Passes
Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic Project 201610628 Switchgear a Advanced D VLSI Circuit	and Protecti rives and C Design sign Estima and Neural 1 and Protecti rives and C Design	on ontrol ation ar Netwo A on ontrol	nd Cos orks	F		OOJA S	60 52 60 51 SIYARAM 60 57	22 19 13 19 17 22		71P 73P 70P 77P 79P		18P	436 #7	Passes
Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic Project 201610628 Switchgear a Advanced D VLSI Circuit Electrical De	and Protecti rives and C Design sign Estima and Neural 1 and Protecti rives and C Design sign Estima	on ontrol ation ar Netwo A on ontrol	nd Cos orks nd Cos	F		OOJA S	60 52 60 51 SIYARAM 60 57 57	22 19 13 19 19 17 22 23		71P 73P 70P 77P 79P 80P		18P 22P	436 #7	Passes
Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic Project 201610628 Switchgear a Advanced D VLSI Circuit	and Protecti rives and C Design sign Estima and Neural 1 and Protecti rives and C Design sign Estima	on ontrol ation ar Netwo A on ontrol	nd Cos orks nd Cos	F		OOJA S	60 52 60 51 51 60 57 57 64	22 19 13 19 19 17 22 23 21		71P 73P 70P 70P 77P 79P 80P 85P		18P 22P	436 #7	Passes
	VLSI CircuitElectrical DeFuzzy LogicProjectSwitchgear aAdvanced DVLSI CircuitElectrical DeFuzzy LogicProject201610614Switchgear aAdvanced DVLSI CircuitElectrical DeFuzzy LogicProject201610614Switchgear aAdvanced DVLSI CircuitSmart GridFuzzy LogicProject201610767Switchgear aAdvanced DVLSI CircuitElectrical DeFuzzy LogicProject201610618Switchgear aAdvanced DVLSI CircuitElectrical DeFuzzy LogicProject201610618Switchgear aAdvanced DVLSI CircuitElectrical DeFuzzy LogicFuzzy LogicFuzzy LogicFuzzy LogicFuzzy Logic	VLSI Circuit Design Electrical Design Estimat Fuzzy Logic and Neural Project 201611231 1 Switchgear and Protectia Advanced Drives and Ca VLSI Circuit Design Electrical Design Estimat Fuzzy Logic and Neural Project 201610614 Project 201610614 Switchgear and Protectia Advanced Drives and Ca VLSI Circuit Design Smart Grid Fuzzy Logic and Neural Project 201610767 Switchgear and Protectia Advanced Drives and Ca VLSI Circuit Design Smart Grid Fuzzy Logic and Neural Project 201610767 Switchgear and Protectia Advanced Drives and Ca VLSI Circuit Design Electrical Design Estimat Fuzzy Logic and Neural Project 201610618 1 Switchgear and Protectia Advanced Drives and Ca VLSI Circuit Design <	VLSI Circuit Design state Electrical Design Estimation and Fuzzy Logic and Neural Network Project 201611231 1 Switchgear and Protection Advanced Drives and Control VLSI Circuit Design Electrical Design Estimation and Fuzzy Logic and Neural Network Project 201610614 1 Advanced Drives and Control VLSI Circuit Design Switchgear and Protection Advanced Drives and Control VLSI Circuit Design Smart Grid Fuzzy Logic and Neural Network Project 201610767 1 Advanced Drives and Control VLSI Circuit Design Switchgear and Protection Advanced Drives and Control VLSI Circuit Design Electrical Design Estimation and Fuzzy Logic and Neural Network Project 201610618 1 Advanced Drives and Control VLSI Circuit Design Electrical Design Estimation and Fuzzy Logic and Neural Network Project	VLSI Circuit Design Electrical Design Estimation and Cos Fuzzy Logic and Neural Networks Project 201611231 1 Switchgear and Protection Advanced Drives and Control VLSI Circuit Design Electrical Design Estimation and Cos Fuzzy Logic and Neural Networks Project 201610614 1 Advanced Drives and Control VLSI Circuit Design Switchgear and Protection Advanced Drives and Control VLSI Circuit Design Smart Grid Fuzzy Logic and Neural Networks Project 201610767 1 A Switchgear and Protection Advanced Drives and Control VLSI Circuit Design Electrical Design Estimation and Cos Fuzzy Logic and Neural Networks Project 201610618 1 A Switchgear and Protection Advanced Drives and Control VLSI Circuit Design Electrical Design Estimation and Cos Fuzzy Logic and Neural Networks <	VLSI Circuit DesignElectrical Design Estimation and CostingFuzzy Logic and Neural NetworksProject2016112311AMSwitchgear and ProtectionAdvanced Drives and ControlVLSI Circuit DesignElectrical Design Estimation and CostingFuzzy Logic and Neural NetworksProject2016106141AMSwitchgear and ProtectionAdvanced Drives and ControlVLSI Circuit DesignFuzzy Logic and Neural NetworksProject2016106141AMSwitchgear and ProtectionAdvanced Drives and ControlVLSI Circuit DesignSmart GridFuzzy Logic and Neural NetworksProject2016107671AFSwitchgear and ProtectionAdvanced Drives and ControlVLSI Circuit DesignElectrical Design Estimation and CostingFuzzy Logic and Neural NetworksProject2016106181AFSwitchgear and ProtectionKuschgear and ProtectionAdvanced Drives and ControlVLSI Circuit DesignElectrical Design Estimation and CostingFuzzy Logic and Neural NetworksProject2016106181AKadvanced Drives and ControlVLSI Circuit DesignElectrical Design Estimation and CostingFuzzy Logic and Neural NetworksProject2016106181Switchgear and Protection	VLSI Circuit Design Electrical Design Estimation and Costing Fuzzy Logic and Neural Networks Project 201611231 1 A M PARAB AJIN Switchgear and Protection Advanced Drives and Control VLSI Circuit Design Electrical Design Estimation and Costing Fuzzy Logic and Neural Networks Project 201610614 1 A M PATIL SAUR Switchgear and Protection Advanced Drives and Control VLSI Circuit Design Switchgear and Protection Advanced Drives and Control VLSI Circuit Design Smart Grid Fuzzy Logic and Neural Networks Project 201610767 1 A F PRATIKSHA Switchgear and Protection Advanced Drives and Control VLSI Circuit Design Electrical Design Estimation and Costing Fuzzy Logic and Neural Networks Project 201610767 1 A F SAKROJI VE Switchgear and Protection Advanced Drives and Control VLSI Circuit Design	VLSI Circuit Design Electrical Design Estimation and Costing Fuzzy Logic and Neural Networks Project 201611231 1 A M PARAB AJINKYA BA Switchgear and Protection Advanced Drives and Control VLSI Circuit Design Electrical Design Estimation and Costing Fuzzy Logic and Neural Networks Project 201610614 1 A M PATIL SAURABH PA Switchgear and Protection Advanced Drives and Control VLSI Circuit Design Switchgear and Protection Advanced Drives and Control VLSI Circuit Design Smart Grid Fuzzy Logic and Neural Networks Project 201610767 1 A F PRATIKSHA NAGRA Switchgear and Protection Advanced Drives and Control VLSI Circuit Design Electrical Design Estimation and Costing Fuzzy Logic and Neural Networks Project 201610767 1 A F SAKROJI VEDA VIN Switchgear and Protection Advanced Drives and Control VLSI Circuit Design </td <td>VLSI Circuit Design 58 Electrical Design Estimation and Costing 55 Fuzzy Logic and Neural Networks 60 Project 61 201611231 1 A M PARAB AJINKYA BALA Switchgear and Protection 64 Advanced Drives and Control 50 VLSI Circuit Design 45 Electrical Design Estimation and Costing 41 Fuzzy Logic and Neural Networks 60 Project 61 201610614 1 A M Switchgear and Protection 66 Advanced Drives and Control 85 VLSI Circuit Design 77 Smart Grid 74 Fuzzy Logic and Neural Networks 65 Project 65 VLSI Circuit Design 74 Switchgear and Protection 82 VLSI Circuit Design 62 Electrical Design Estimation and Costing 63 Project 61 Advanced Drives and Control 82 VLSI Circuit Design 62 Electrical Design Estimation and Costing</td> <td>VLSI Circuit Design Estimation and Costing 58 16 Electrical Design Estimation and Costing 55 17 Fuzzy Logic and Neural Networks 60 18 Project 60 18 201611231 1 A M PARAB AJINKYA BLA Switchgear and Protection 64 12 Advanced Drives and Control 50 17 VLSI Circuit Design Estimation and Costing 41 10 Fuzzy Logic and Neural Networks 60 13 Project 66 19 201610614 1 A M PATIL SAURABH PARASHARAM Switchgear and Protection 66 19 Advanced Drives and Control 85 24 VLSI Circuit Design 77 22 Smart Grid 77 22 Fuzzy Logic and Neural Networks 65 21 Project 65 21 201610767 1 A F PRATIKSHA NAGRAJ PATIL Switchgear and Protection 82 19 11 Advanced Drives and Control 82 19</td> <td>VLSI Circuit Design 58 16 Electrical Design Estimation and Costing 55 17 Fuzzy Logic and Neural Networks 60 18 Project 60 18 201611231 1 A M PARAB AJINKYA BALA Switchgear and Protection 64 12 Advanced Drives and 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COLLEGE WISE STUDENT REVALUATION RESULT REGISTER FOR SEMESTER-VII EXAM

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Bachelor of Engineering (Electrical & Electronics) RC 2016-17 College :-Government of Goa College of Engineering EXAM YEAR:November 2019 SEAT NO P.R. NO. ATTEMPTS ARE PRO SEX NAME OF THE THEORY SESSIONAL TERM WORK TOTAL PRACTICAL ORAL TOTAL CANDIDATE 125 OR 150 REMARKS CD SEM MAX. 50 OR 25 50 OR 25 725 100 25 25 50 OR 60 25 OR 10 25 OR 10 290 ESTE MIN. 40 R-VII 190581 201610632 1 A Μ SANGUEKAR PRASANN PRADIP 70 15 85P Switchgear and Protection 87 22 109P Advanced Drives and Control 75 20 95P 22P VLSI Circuit Design 72 20 92P 19P **Electrical Design Estimation and Costing** 94P 16P 75 19 Fuzzy Logic and Neural Networks 22P Project 554 #7 Passes 190582 201711962 1 A Μ SAWANT KAUSHAL RAVI 68 19 87P Switchgear and Protection 81 22 103P Advanced Drives and Control 72 92P 20P **VLSI** Circuit Design 20 **Electrical Design Estimation and Costing** 74 23 97P 20P 19P 77 18 95P Fuzzy Logic and Neural Networks 23P Project 556 Passes 190583 SAWANT VISHRAM ATMA 201610639 1 A Μ 67 13 80P Switchgear and Protection 21 82 103F Advanced Drives and Control 12 20P 44 56P VLSI Circuit Design 61 17 78P 16P **Electrical Design Estimation and Costing** 56 13 69P 18P Fuzzy Logic and Neural Networks 22P Project 462 #7 Passes A SHAIK MOHAMMAD SAFIR 190584 201704891 1 Μ **RESULT WITHHELD-(SEM.5 NOT PASSED)** 40 12 52P Switchgear and Protection Advanced Drives and Control 63 14 77P 35\$5 14 49P \$5 23P VLSI Circuit Design Electrical Design Estimation and Costing 47 12 59P 19P 10 60P 18P 50 Fuzzy Logic and Neural Networks 24P Project 190585 Μ SHET PARSEKAR PRANAV VITHAL PRAKASH 201704890 1 A Switchgear and Protection 66 14 80P 50 Advanced Drives and Control 16 66P 35\$5 10 45P \$5 12P VLSI Circuit Design 58 12 70P 14P **Electrical Design Estimation and Costing** 58 12 70P 17P Fuzzy Logic and Neural Networks 22P Project 396 \$5 Passes 190586 201610642 1 Α Μ SHETKER RUTHVIK EKNATH 68 12 80P Switchgear and Protection 57 18 75P Advanced Drives and Control 47 12 59P 15P **VLSI** Circuit Design 54 11 65P 15P **Electrical Design Estimation and Costing** 57 11 68P 14P Fuzzy Logic and Neural Networks 19P Project 410 Passes 190587 Μ 201610744 1 Α SUHAIL FIROZ KHAN 72 19 91P Switchgear and Protection

Advanced Drives and Control	82	25	107P				
VLSI Circuit Design	80	17	97P	21P			
Smart Grid	82	20	102P		21P		
Fuzzy Logic and Neural Networks	83	23	106P		24P		
Project					22P		
						591	Passes

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Bachelor of Engineering (Electrical & Electronics) RC 2016-17 College :-Government of Goa College of Engineering EXAM YEAR:November 2019 SEAT NO P.R. NO. ATTEMPTS ARE PRO SEX NAME OF THE THEORY SESSIONAL TERM WORK TOTAL PRACTICAL ORAL TOTAL 725 290 SEM CANDIDATE MAX. 125 OR 150 50 OR 25 50 OR 25 REMARKS CD 100 25 25 50 OR 60 ESTE 40 25 OR 10 25 OR 10 MIN. R-VII 190589 F 201610762 1 Α SWASTIKA Switchgear and Protection 40 12 52P 76 18 94P Advanced Drives and Control 52 18 70P 21P **VLSI** Circuit Design 57 13 70P 16P **Electrical Design Estimation and Costing** 60 18 78P 23P Fuzzy Logic and Neural Networks 22P Project 446 Passes TORASKAR VAISHNAVI KRISHNA 190591 201610647 A F. 1 74 15 89P Switchgear and Protection 80 21 101P Advanced Drives and Control 67 88P 18P VLSI Circuit Design 21 14 82P 20P Smart Grid 68 21 79P 20P Fuzzy Logic and Neural Networks 58 23P Project 520 Passes 190592 201610649 A Μ VAZ KEWEL 1 66 12 78P Switchgear and Protection 80 22 Advanced Drives and Control 102P 70 19 89P 20P VLSI Circuit Design 73 12 85P 18P Smart Grid 91P 23P 71 20 Fuzzy Logic and Neural Networks 24P Project 530 #7 Passes 190593 A 201610651 1 Μ VERENKER SHUBHAM ANIL Switchgear and Protection 58 15 73P Advanced Drives and Control 76 23 99P 60 18 78P 13P **VLSI** Circuit Design Electrical Design Estimation and Costing 54 17 71P 19P 56 16 72P 17P Fuzzy Logic and Neural Networks 20P Project 462 Passes

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P:Passes; F:Fails; Abs:Absent; N:Non Appearance; D:First Class with Distinction; I: First Class; S:Second Class; NE:Not Eligible; +:Marks carried over Discrepancy If any in the Result shall be informed to the University within a week from the date of receipt of the result by the colleges.