



Goa University
Taleigao Plateau, Goa - 403206
Result Register Report
Bachelor of Engineering(RC19-20) CBCS

1 of 1

College Name: Government of Goa College of Engineering
Branch Name: Electronics (VLSI Design & Technology)
Examination : Semester V May 2025

1 **Seat No:** 50229 **PR Number:** 202211694 **Name:** ASHWIN NARESH MALIK

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	51 +	9 +	15 +	75 +			75	B	6	24
VLSI 520	Analog VLSI Design	4	72 +	10 +	18 +	100 +			100	A	8	32
VLSI 531	Computer Organization and Design	3	66 +	12 +		78 +			78	B+	7	21
VLSI 541	Soft Computing	3	54 +	10 +		64 +			64	B	6	18
VLSI 550	Digital VLSI Design Lab	1			10 +	10 +	18+		28	B+	7	7
VLSI 560	Analog VLSI Design Lab	1			17 +	17 +	22+		39	A+	9	9
VLSI 570	HDL Lab	2			42 +	42 +	16+		58	A+	9	18
HM009	Ethics and Entrepreneurship	3	47 +	8 +		55 +			55	P	4	12
IT534	Internet of Things	3	52 P	7 +		59 P			59	C	5	15

Total Weighted Grade Points : 156

Entitlement Marks : --

Earned Credits: 24

SGPA : 6.5*

2 **Seat No:** 50230 **PR Number:** 202211494 **Name:** JOHAN JOAQUIM FERNANDES

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	64 P	10 +	12 +	86 P			86	B+	7	28
VLSI 520	Analog VLSI Design	4	63 +	6 +	15 +	84 +			84	B+	7	28
VLSI 531	Computer Organization and Design	3	74 +	15 +		89 +			89	A	8	24
VLSI 541	Soft Computing	3	60 +	12 +		72 +			72	B+	7	21
VLSI 550	Digital VLSI Design Lab	1			19 +	19 +	18+		37	A	8	8
VLSI 560	Analog VLSI Design Lab	1			19 +	19 +	23+		42	A+	9	9
VLSI 570	HDL Lab	2			28 +	28 +	22+		50	A	8	16
HM009	Ethics and Entrepreneurship	3	78 +	17 +		95 +			95	A+	9	27
IT534	Internet of Things	3	69 +	14 +		83 +			83	A	8	24

Total Weighted Grade Points : 185

Entitlement Marks : --

Earned Credits: 24

SGPA : 7.71*

3 **Seat No:** 50231 **PR Number:** 202309926 **Name:** PRASHANTH RAJESH MARATHE

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	43 +	10 +	10 +	63 +			63	P	4	16
VLSI 520	Analog VLSI Design	4	75 +	6 +	14 +	95 +			95	B+	7	28
VLSI 531	Computer Organization and Design	3	50 +	11 +		61 +			61	C	5	15
VLSI 541	Soft Computing	3	51 P	13 +		64 P			64	B	6	18
VLSI 550	Digital VLSI Design Lab	1			15 +	15 +	12+		27	B	6	6
VLSI 560	Analog VLSI Design Lab	1			18 +	18 +	22+		40	A+	9	9
VLSI 570	HDL Lab	2			27 +	27 +	18+		45	B+	7	14
ET544	Electronic Instrumentation and Automation	3	53 +	14 +		67 +			67	B	6	18
HM009	Ethics and Entrepreneurship	3	40 +	10 +		50 +			50	P	4	12

Total Weighted Grade Points : 136

Entitlement Marks : --

Earned Credits: 24

SGPA : 5.67*

4 **Seat No:** 50232 **PR Number:** 202211695 **Name:** RUTAMBAR DILIP DESAI

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	Ab	8 +	12 +	20 F			20	Ab	0	0
VLSI 520	Analog VLSI Design	4	67 P	8 +	15 +	90 P			90	B+	7	28
VLSI 531	Computer Organization and Design	3	56 +	14 +		70 +			70	B+	7	21
VLSI 541	Soft Computing	3	55 +	10 +		65 +			65	B	6	18
VLSI 550	Digital VLSI Design Lab	1			16 +	16 +	12+		28	B+	7	7
VLSI 560	Analog VLSI Design Lab	1			17 +	17 +	20+		37	A	8	8
VLSI 570	HDL Lab	2			40 +	40 +	20+		60	A+	9	18
CV543	Green Building	3	66 +	24 +		90 +			90	A	8	24
HM009	Ethics and Entrepreneurship	3	51 +	17 +		68 +			68	B	6	18

Total Weighted Grade Points : 142

Entitlement Marks : # 10

Earned Credits: 20

SGPA : --

Read By:

Checked By:

Date : 11 AUG 2025

Asstt. Registrar Exam(Prof)
(Technical and Allied)

Asstt. Registrar Exam

Controller of Examinations