



Goa University
Taleigao Plateau, Goa - 403206
Result Register Report
Bachelor of Engineering(RC19-20) CBCS

1 of 11

College Name: Government of Goa College of Engineering
Branch Name: Electronics (VLSI Design & Technology)
Examination : Semester VI May 2025

1 **Seat No:** 60798 **PR Number:** 202211433 **Name:** AKSHAY SANJEEV KULKARNI

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	61 P	20		81 P			81	B+	7	21
VLSI 620	Analog and Digital Communication	3	64 P	21		85 P			85	A	8	24
VLSI 633	Introduction to Micro-electromechanical Systems	3	66 P	14		80 P			80	B+	7	21
VLSI 641	Elements of Radar System Engineering	3	73 P	20		93 P			93	A	8	24
VLSI 650	Embedded Design Lab	1			22 P	22 P	22P		44	O	10	10
VLSI 660	Programming Lab II	1			23 P	23 P	21P		44	O	10	10
VLSI 670	FPGA Design Lab	2			47 P	47 P	23P		70	O	10	20
CV633	Air and Noise Pollution and Control	3	53 P	19		72 P			72	B+	7	21
HM006	Cyber Law and IPR	3	69 P	20		89 P			89	A	8	24
@IOT-03	Ubiquitous Sensing, Computing And Communication	4	45 P	16		61 P	23P		84	B+	7	28

Total Weighted Grade Points : 175

Entitlement Marks : --

Earned Credits: 22

SGPA : 7.95

2 **Seat No:** 60799 **PR Number:** 202211322 **Name:** ANKIT RAKESH MISHRA

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	61 P	10		71 P			71	B+	7	21
VLSI 620	Analog and Digital Communication	3	52 P	10		62 P			62	C	5	15
VLSI 633	Introduction to Micro-electromechanical Systems	3	57 P	16		73 P			73	B+	7	21
VLSI 641	Elements of Radar System Engineering	3	79 P	19		98 P			98	A+	9	27
VLSI 650	Embedded Design Lab	1			18 P	18 P	20P		38	A+	9	9
VLSI 660	Programming Lab II	1			15 P	15 P	20P		35	A	8	8
VLSI 670	FPGA Design Lab	2			42 P	42 P	14P		56	A	8	16
CV633	Air and Noise Pollution and Control	3	55 P	13		68 P			68	B	6	18
HM006	Cyber Law and IPR	3	72 P	20		92 P			92	A	8	24

Total Weighted Grade Points : 159

Entitlement Marks : # 10

Earned Credits: 22

SGPA : 7.23

3 **Seat No:** 60800 **PR Number:** 202211429 **Name:** ARYA ANIL PARAB

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	68 P	21		89 P			89	A	8	24
VLSI 620	Analog and Digital Communication	3	48 P	15		63 P			63	B	6	18
VLSI 633	Introduction to Micro-electromechanical Systems	3	60 P	16		76 P			76	B+	7	21
VLSI 641	Elements of Radar System Engineering	3	72 P	19		91 P			91	A	8	24
VLSI 650	Embedded Design Lab	1			24 P	24 P	22P		46	O	10	10
VLSI 660	Programming Lab II	1			22 P	22 P	22P		44	O	10	10
VLSI 670	FPGA Design Lab	2			46 P	46 P	19P		65	O	10	20
HM006	Cyber Law and IPR	3	53 P	18		71 P			71	B+	7	21
IT643	Computer Forensics and Cyber Security	3	72 P	14		86 P			86	A	8	24

Total Weighted Grade Points : 172

Entitlement Marks : --

Earned Credits: 22

SGPA : 7.82

4 **Seat No:** 60801 **PR Number:** 202211636 **Name:** ARYA RAJESH BHATKANDE

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	68 P	10		78 P			78	B+	7	21
VLSI 620	Analog and Digital Communication	3	47 P	10		57 P			57	C	5	15
VLSI 633	Introduction to Micro-electromechanical Systems	3	46 P	16		62 P			62	C	5	15
VLSI 641	Elements of Radar System Engineering	3	72 P	17		89 P			89	A	8	24
VLSI 650	Embedded Design Lab	1			17 P	17 P	20P		37	A	8	8
VLSI 660	Programming Lab II	1			15 P	15 P	18P		33	A	8	8
VLSI 670	FPGA Design Lab	2			38 P	38 P	13P		51	A	8	16
CV633	Air and Noise Pollution and Control	3	64 P	13		77 P			77	B+	7	21
HM006	Cyber Law and IPR	3	62 P	20		82 P			82	A	8	24

Total Weighted Grade Points : 152

Entitlement Marks : --

Earned Credits: 22

SGPA : 6.91



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College Name: Government of Goa College of Engineering
Branch Name: Electronics (VLSI Design & Technology)
Examination : Semester VI May 2025

5 **Seat No:** 60802 **PR Number:** 202211391 **Name:** ARYAN KAUSHAL MISHRA

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	74 P	19		93 P			93	A	8	24
VLSI 620	Analog and Digital Communication	3	61 P	13		74 P			74	B+	7	21
VLSI 633	Introduction to Micro-electromechanical Systems	3	58 P	19		77 P			77	B+	7	21
VLSI 641	Elements of Radar System Engineering	3	72 P	21		93 P			93	A	8	24
VLSI 650	Embedded Design Lab	1			22 P	22 P	19P		41	A+	9	9
VLSI 660	Programming Lab II	1			17 P	17 P	20P		37	A	8	8
VLSI 670	FPGA Design Lab	2			42 P	42 P	20P		62	A+	9	18
CV633	Air and Noise Pollution and Control	3	65 P	14		79 P			79	B+	7	21
HM006	Cyber Law and IPR	3	68 P	22		90 P			90	A	8	24

Total Weighted Grade Points : 170

Entitlement Marks : --

Earned Credits: 22

SGPA : 7.73

6 **Seat No:** 60803 **PR Number:** 202211694 **Name:** ASHWIN NARESH MALIK

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	44 P	10		54 P			54	P	4	12
VLSI 620	Analog and Digital Communication	3	40 P	10		50 P			50	P	4	12
VLSI 633	Introduction to Micro-electromechanical Systems	3	40 P	9		49 F			49	F	0	0
VLSI 641	Elements of Radar System Engineering	3	58 P	17		75 P			75	B+	7	21
VLSI 650	Embedded Design Lab	1			10 P	10 P	19P		29	B+	7	7
VLSI 660	Programming Lab II	1			10 P	10 P	15P		25	B	6	6
VLSI 670	FPGA Design Lab	2			35 P	35 P	11P		46	B+	7	14
HM006	Cyber Law and IPR	3	61 P	11		72 P			72	B+	7	21
IT643	Computer Forensics and Cyber Security	3	71 P	12		83 P			83	A	8	24

Total Weighted Grade Points : 117

Entitlement Marks : --

Earned Credits: 19

SGPA : --

7 **Seat No:** 60804 **PR Number:** 202211430 **Name:** DIVYA NAGESH PRABHU

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	87 P	24		111 P			111	O	10	30
VLSI 620	Analog and Digital Communication	3	80 P	23		103 P			103	A+	9	27
VLSI 633	Introduction to Micro-electromechanical Systems	3	86 P	25		111 P			111	O	10	30
VLSI 641	Elements of Radar System Engineering	3	91 P	25		116 P			116	O	10	30
VLSI 650	Embedded Design Lab	1			22 P	22 P	22P		44	O	10	10
VLSI 660	Programming Lab II	1			25 P	25 P	23P		48	O	10	10
VLSI 670	FPGA Design Lab	2			47 P	47 P	25P		72	O	10	20
CV633	Air and Noise Pollution and Control	3	65 P	22		87 P			87	A	8	24
HM006	Cyber Law and IPR	3	73 P	22		95 P			95	A+	9	27
@CEAM-03	Data Science and Analytics	4	56 P	17		73 P		24 P	97	B+	7	28

Total Weighted Grade Points : 208

Entitlement Marks : --

Earned Credits: 22

SGPA : 9.45

8 **Seat No:** 60805 **PR Number:** 202211610 **Name:** DIVYA SIROBHUSHANAM

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	57 P	12		69 P			69	B+	7	21
VLSI 620	Analog and Digital Communication	3	49 P	14		63 P			63	B	6	18
VLSI 633	Introduction to Micro-electromechanical Systems	3	64 P	17		81 P			81	B+	7	21
VLSI 641	Elements of Radar System Engineering	3	78 P	23		101 P			101	A+	9	27
VLSI 650	Embedded Design Lab	1			15 P	15 P	16P		31	B+	7	7
VLSI 660	Programming Lab II	1			18 P	18 P	19P		37	A	8	8
VLSI 670	FPGA Design Lab	2			42 P	42 P	17P		59	A+	9	18
EE644	Electrical Energy Conservation & Auditing	3	60 P	14		74 P			74	B+	7	21
HM006	Cyber Law and IPR	3	57 P	10		67 P			67	B	6	18

Total Weighted Grade Points : 159

Entitlement Marks : --

Earned Credits: 22

SGPA : 7.23



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College Name: Government of Goa College of Engineering
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Examination : Semester VI May 2025

9 **Seat No:** 60806 **PR Number:** 202211306 **Name:** DOBAASHISH DEV

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	45 P	10		55 P			55	P	4	12
VLSI 620	Analog and Digital Communication	3	42 P	11		53 P			53	P	4	12
VLSI 633	Introduction to Micro-electromechanical Systems	3	53 P	10		63 P			63	B	6	18
VLSI 641	Elements of Radar System Engineering	3	84 P	18		102 P			102	A+	9	27
VLSI 650	Embedded Design Lab	1			11 P	11 P	16P		27	B	6	6
VLSI 660	Programming Lab II	1			17 P	17 P	17P		34	A	8	8
VLSI 670	FPGA Design Lab	2			40 P	40 P	12P		52	A	8	16
HM006	Cyber Law and IPR	3	70 P	14		84 P			84	A	8	24
IT643	Computer Forensics and Cyber Security	3	72 P	14		86 P			86	A	8	24

Total Weighted Grade Points : 147

Entitlement Marks : --

Earned Credits: 22

SGPA : 6.68

10 **Seat No:** 60807 **PR Number:** 202211938 **Name:** GILBILE VEDANT LAXMAN

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	48 P	10		58 P			58	C	5	15
VLSI 620	Analog and Digital Communication	3	30 F	10		40 F			40	F	0	0
VLSI 633	Introduction to Micro-electromechanical Systems	3	43 P	10		53 P			53	P	4	12
VLSI 641	Elements of Radar System Engineering	3	60 P	14		74 P			74	B+	7	21
VLSI 650	Embedded Design Lab	1			14 P	14 P	17P		31	B+	7	7
VLSI 660	Programming Lab II	1			18 P	18 P	18P		36	A	8	8
VLSI 670	FPGA Design Lab	2			40 P	40 P	14P		54	A	8	16
HM006	Cyber Law and IPR	3	65 P	16		81 P			81	B+	7	21
IT644	E Commerce	3	33 F	12		45 F			45	F	0	0

Total Weighted Grade Points : 100

Entitlement Marks : --

Earned Credits: 16

SGPA : --

11 **Seat No:** 60808 **PR Number:** 202211517 **Name:** GREESHMA HARISH NAYAK

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	66 P	19		85 P			85	A	8	24
VLSI 620	Analog and Digital Communication	3	52 P	18		70 P			70	B+	7	21
VLSI 633	Introduction to Micro-electromechanical Systems	3	61 P	24		85 P			85	A	8	24
VLSI 641	Elements of Radar System Engineering	3	74 P	21		95 P			95	A+	9	27
VLSI 650	Embedded Design Lab	1			21 P	21 P	21P		42	A+	9	9
VLSI 660	Programming Lab II	1			22 P	22 P	22P		44	O	10	10
VLSI 670	FPGA Design Lab	2			46 P	46 P	19P		65	O	10	20
HM006	Cyber Law and IPR	3	67 P	18		85 P			85	A	8	24
IT643	Computer Forensics and Cyber Security	3	69 P	16		85 P			85	A	8	24

Total Weighted Grade Points : 183

Entitlement Marks : --

Earned Credits: 22

SGPA : 8.32

12 **Seat No:** 60809 **PR Number:** 202211663 **Name:** HARISH HANUMANT NAIK

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	51 P	14		65 P			65	B	6	18
VLSI 620	Analog and Digital Communication	3	45 P	15		60 P			60	C	5	15
VLSI 633	Introduction to Micro-electromechanical Systems	3	58 P	16		74 P			74	B+	7	21
VLSI 641	Elements of Radar System Engineering	3	73 P	18		91 P			91	A	8	24
VLSI 650	Embedded Design Lab	1			20 P	20 P	19P		39	A+	9	9
VLSI 660	Programming Lab II	1			21 P	21 P	20P		41	A+	9	9
VLSI 670	FPGA Design Lab	2			44 P	44 P	14P		58	A+	9	18
CV633	Air and Noise Pollution and Control	3	59 P	20		79 P			79	B+	7	21
HM006	Cyber Law and IPR	3	60 P	22		82 P			82	A	8	24

Total Weighted Grade Points : 159

Entitlement Marks : --

Earned Credits: 22

SGPA : 7.23



College Name: Government of Goa College of Engineering
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13 **Seat No:** 60810 **PR Number:** 202211494 **Name:** JOHAN JOAQUIM FERNANDES

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	51 P	14		65 P			65	B	6	18
VLSI 620	Analog and Digital Communication	3	52 P	13		65 P			65	B	6	18
VLSI 633	Introduction to Micro-electromechanical Systems	3	49 P	15		64 P			64	B	6	18
VLSI 641	Elements of Radar System Engineering	3	64 P	16		80 P			80	B+	7	21
VLSI 650	Embedded Design Lab	1			18 P	18 P	20P		38	A+	9	9
VLSI 660	Programming Lab II	1			17 P	17 P	19P		36	A	8	8
VLSI 670	FPGA Design Lab	2			42 P	42 P	12P		54	A	8	16
HM006	Cyber Law and IPR	3	54 P	13		67 P			67	B	6	18
IT643	Computer Forensics and Cyber Security	3	64 P	13		77 P			77	B+	7	21

Total Weighted Grade Points : 147

Entitlement Marks : --

Earned Credits: 22

SGPA : 6.68

14 **Seat No:** 60811 **PR Number:** 202211453 **Name:** KANISHKA KIRAN MANDREKAR

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	70 P	20		90 P			90	A	8	24
VLSI 620	Analog and Digital Communication	3	57 P	15		72 P			72	B+	7	21
VLSI 633	Introduction to Micro-electromechanical Systems	3	68 P	22		90 P			90	A	8	24
VLSI 641	Elements of Radar System Engineering	3	88 P	21		109 P			109	O	10	30
VLSI 650	Embedded Design Lab	1			20 P	20 P	21P		41	A+	9	9
VLSI 660	Programming Lab II	1			21 P	21 P	19P		40	A+	9	9
VLSI 670	FPGA Design Lab	2			40 P	40 P	12P		52	A	8	16
HM006	Cyber Law and IPR	3	60 P	22		82 P			82	A	8	24
IT643	Computer Forensics and Cyber Security	3	60 P	13		73 P			73	B+	7	21

Total Weighted Grade Points : 178

Entitlement Marks : --

Earned Credits: 22

SGPA : 8.09

15 **Seat No:** 60812 **PR Number:** 202311062 **Name:** KUNDAIKER MADHUKER NANU

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	47 P	12		59 P			59	C	5	15
VLSI 620	Analog and Digital Communication	3	40 P	10		50 P			50	P	4	12
VLSI 633	Introduction to Micro-electromechanical Systems	3	50 P	19		69 P			69	B+	7	21
VLSI 641	Elements of Radar System Engineering	3	77 P	17		94 P			94	A+	9	27
VLSI 650	Embedded Design Lab	1			19 P	19 P	17P		36	A	8	8
VLSI 660	Programming Lab II	1			21 P	21 P	18P		39	A+	9	9
VLSI 670	FPGA Design Lab	2			44 P	44 P	16P		60	A+	9	18
CV633	Air and Noise Pollution and Control	3	50 P	14		64 P			64	B	6	18
HM006	Cyber Law and IPR	3	52 P	16		68 P			68	B	6	18

Total Weighted Grade Points : 146

Entitlement Marks : --

Earned Credits: 22

SGPA : 6.64

16 **Seat No:** 60813 **PR Number:** 202211518 **Name:** MALLIKARJUN BASAPPA GAUD

(RESULT WITHHELD)

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points

Total Weighted Grade Points : --

Entitlement Marks : --

Earned Credits: --

SGPA : --



College Name: Government of Goa College of Engineering
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17 **Seat No:** 60814 **PR Number:** 202211638 **Name:** MALVIKA GAJANAN KORGAONKER

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	75 P	19		94 P			94	A+	9	27
VLSI 620	Analog and Digital Communication	3	57 P	21		78 P			78	B+	7	21
VLSI 633	Introduction to Micro-electromechanical Systems	3	73 P	21		94 P			94	A+	9	27
VLSI 641	Elements of Radar System Engineering	3	78 P	22		100 P			100	A+	9	27
VLSI 650	Embedded Design Lab	1			20 P	20 P	20P		40	A+	9	9
VLSI 660	Programming Lab II	1			23 P	23 P	18P		41	A+	9	9
VLSI 670	FPGA Design Lab	2			46 P	46 P	18P		64	O	10	20
CV633	Air and Noise Pollution and Control	3	53 P	19		72 P			72	B+	7	21
HM006	Cyber Law and IPR	3	73 P	19		92 P			92	A	8	24

Total Weighted Grade Points : 185

Entitlement Marks : --

Earned Credits: 22

SGPA : 8.41

18 **Seat No:** 60815 **PR Number:** 202311061 **Name:** MANDREKAR BALKRISHANA SHASHIKANT

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	41 P	14		55 P			55	P	4	12
VLSI 620	Analog and Digital Communication	3	52 P	10		62 P			62	C	5	15
VLSI 633	Introduction to Micro-electromechanical Systems	3	27 F	13		40 F			40	F	0	0
VLSI 641	Elements of Radar System Engineering	3	67 P	17		84 P			84	A	8	24
VLSI 650	Embedded Design Lab	1			19 P	19 P	15P		34	A	8	8
VLSI 660	Programming Lab II	1			21 P	21 P	21P		42	A+	9	9
VLSI 670	FPGA Design Lab	2			45 P	45 P	20P		65	O	10	20
EE644	Electrical Energy Conservation & Auditing	3	48 P	10		58 P			58	C	5	15
HM006	Cyber Law and IPR	3	48 P	16		64 P			64	B	6	18

Total Weighted Grade Points : 121

Entitlement Marks : --

Earned Credits: 19

SGPA : --

19 **Seat No:** 60816 **PR Number:** 202211293 **Name:** MITHILA HARSHAD VELINGKAR

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	42 P	11		53 P			53	P	4	12
VLSI 620	Analog and Digital Communication	3	27 F	11		38 F			38	F	0	0
VLSI 633	Introduction to Micro-electromechanical Systems	3	40 P	14		54 P			54	P	4	12
VLSI 641	Elements of Radar System Engineering	3	69 P	16		85 P			85	A	8	24
VLSI 650	Embedded Design Lab	1			17 P	17 P	18P		35	A	8	8
VLSI 660	Programming Lab II	1			19 P	19 P	20P		39	A+	9	9
VLSI 670	FPGA Design Lab	2			43 P	43 P	15P		58	A+	9	18
CV633	Air and Noise Pollution and Control	3	44 P	9		53 P			53	P	4	12
HM006	Cyber Law and IPR	3	63 P	14		77 P			77	B+	7	21

Total Weighted Grade Points : 116

Entitlement Marks : --

Earned Credits: 19

SGPA : --

20 **Seat No:** 60817 **PR Number:** 202211519 **Name:** NARAYAN SHANKER SAWANT

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	56 P	12		68 P			68	B	6	18
VLSI 620	Analog and Digital Communication	3	51 P	10		61 P			61	C	5	15
VLSI 633	Introduction to Micro-electromechanical Systems	3	57 P	15		72 P			72	B+	7	21
VLSI 641	Elements of Radar System Engineering	3	69 P	16		85 P			85	A	8	24
VLSI 650	Embedded Design Lab	1			12 P	12 P	20P		32	B+	7	7
VLSI 660	Programming Lab II	1			16 P	16 P	18P		34	A	8	8
VLSI 670	FPGA Design Lab	2			35 P	35 P	11P		46	B+	7	14
EE643	Hybrid Vehicles	3	52 P	13		65 P			65	B	6	18
HM006	Cyber Law and IPR	3	41 P	14		55 P			55	P	4	12

Total Weighted Grade Points : 137

Entitlement Marks : # 12

Earned Credits: 22

SGPA : 6.23



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College Name: Government of Goa College of Engineering
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21 **Seat No:** 60818 **PR Number:** 202309925 **Name:** NIGEL MENDES

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	41 P	10		51 P			51	P	4	12
VLSI 620	Analog and Digital Communication	3	40 P	10		50 P			50	P	4	12
VLSI 633	Introduction to Micro-electromechanical Systems	3	29 F	10		39 F			39	F	0	0
VLSI 641	Elements of Radar System Engineering	3	73 P	12		85 P			85	A	8	24
VLSI 650	Embedded Design Lab	1			16 P	16 P	16P		32	B+	7	7
VLSI 660	Programming Lab II	1			17 P	17 P	17P		34	A	8	8
VLSI 670	FPGA Design Lab	2			28 P	28 P	11P		39	B	6	12
HM006	Cyber Law and IPR	3	61 P	16		77 P			77	B+	7	21
ME644	Supply Chain Management	3	69 P	20		89 P			89	A	8	24

Total Weighted Grade Points : 120

Entitlement Marks : --

Earned Credits: 19

SGPA : --

22 **Seat No:** 60819 **PR Number:** 202211432 **Name:** PRASAD BALAJI ST TOKARI KOLI

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	79 P	21		100 P			100	A+	9	27
VLSI 620	Analog and Digital Communication	3	54 P	18		72 P			72	B+	7	21
VLSI 633	Introduction to Micro-electromechanical Systems	3	65 P	18		83 P			83	A	8	24
VLSI 641	Elements of Radar System Engineering	3	85 P	22		107 P			107	O	10	30
VLSI 650	Embedded Design Lab	1			22 P	22 P	22P		44	O	10	10
VLSI 660	Programming Lab II	1			23 P	23 P	18P		41	A+	9	9
VLSI 670	FPGA Design Lab	2			47 P	47 P	22P		69	O	10	20
CV633	Air and Noise Pollution and Control	3	42 P	14		56 P			56	P	4	12
HM006	Cyber Law and IPR	3	69 P	20		89 P			89	A	8	24
@IOT-03	Ubiquitous Sensing, Computing And Communication	4	59 P	17		76 P	23P		99	A	8	32

Total Weighted Grade Points : 177

Entitlement Marks : --

Earned Credits: 22

SGPA : 8.05

23 **Seat No:** 60820 **PR Number:** 202309926 **Name:** PRASHANTH RAJESH MARATHE

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	40 P	12		52 P			52	P	4	12
VLSI 620	Analog and Digital Communication	3	40 P	10		50 P			50	P	4	12
VLSI 633	Introduction to Micro-electromechanical Systems	3	43 P	10		53 P			53	P	4	12
VLSI 641	Elements of Radar System Engineering	3	74 P	18		92 P			92	A	8	24
VLSI 650	Embedded Design Lab	1			16 P	16 P	19P		35	A	8	8
VLSI 660	Programming Lab II	1			16 P	16 P	17P		33	A	8	8
VLSI 670	FPGA Design Lab	2			28 P	28 P	11P		39	B	6	12
HM006	Cyber Law and IPR	3	70 P	18		88 P			88	A	8	24
ME644	Supply Chain Management	3	43 P	20		63 P			63	B	6	18

Total Weighted Grade Points : 130

Entitlement Marks : --

Earned Credits: 22

SGPA : 5.91

24 **Seat No:** 60821 **PR Number:** 202211567 **Name:** PRERANA RAJENDRA GHADI

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	87 P	24		111 P			111	O	10	30
VLSI 620	Analog and Digital Communication	3	69 P	17		86 P			86	A	8	24
VLSI 633	Introduction to Micro-electromechanical Systems	3	81 P	19		100 P			100	A+	9	27
VLSI 641	Elements of Radar System Engineering	3	90 P	25		115 P			115	O	10	30
VLSI 650	Embedded Design Lab	1			24 P	24 P	22P		46	O	10	10
VLSI 660	Programming Lab II	1			20 P	20 P	19P		39	A+	9	9
VLSI 670	FPGA Design Lab	2			47 P	47 P	20P		67	O	10	20
HM006	Cyber Law and IPR	3	57 P	22		79 P			79	B+	7	21
IT643	Computer Forensics and Cyber Security	3	76 P	14		90 P			90	A	8	24

Total Weighted Grade Points : 195

Entitlement Marks : --

Earned Credits: 22

SGPA : 8.86



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College Name: Government of Goa College of Engineering
Branch Name: Electronics (VLSI Design & Technology)
Examination : Semester VI May 2025

25 **Seat No:** 60822 **PR Number:** 202211392 **Name:** RIYA P M

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	56 P	13		69 P			69	B+	7	21
VLSI 620	Analog and Digital Communication	3	63 P	10		73 P			73	B+	7	21
VLSI 633	Introduction to Micro-electromechanical Systems	3	68 P	19		87 P			87	A	8	24
VLSI 641	Elements of Radar System Engineering	3	86 P	22		108 P			108	O	10	30
VLSI 650	Embedded Design Lab	1			14 P	14 P	17P		31	B+	7	7
VLSI 660	Programming Lab II	1			17 P	17 P	18P		35	A	8	8
VLSI 670	FPGA Design Lab	2			36 P	36 P	14P		50	A	8	16
EE643	Hybrid Vehicles	3	58 P	14		72 P			72	B+	7	21
HM006	Cyber Law and IPR	3	74 P	17		91 P			91	A	8	24

Total Weighted Grade Points : 172

Entitlement Marks : --

Earned Credits: 22

SGPA : 7.82

26 **Seat No:** 60823 **PR Number:** 202211283 **Name:** ROHAN S JOSEPH

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	41 P	10		51 P			51	P	4	12
VLSI 620	Analog and Digital Communication	3	42 P	10		52 P			52	P	4	12
VLSI 633	Introduction to Micro-electromechanical Systems	3	41 P	11		52 P			52	P	4	12
VLSI 641	Elements of Radar System Engineering	3	64 P	10		74 P			74	B+	7	21
VLSI 650	Embedded Design Lab	1			20 P	20 P	16P		36	A	8	8
VLSI 660	Programming Lab II	1			16 P	16 P	19P		35	A	8	8
VLSI 670	FPGA Design Lab	2			40 P	40 P	12P		52	A	8	16
HM006	Cyber Law and IPR	3	51 P	10		61 P			61	C	5	15
IT643	Computer Forensics and Cyber Security	3	56 P	13		69 P			69	B+	7	21

Total Weighted Grade Points : 125

Entitlement Marks : --

Earned Credits: 22

SGPA : 5.68

27 **Seat No:** 60824 **PR Number:** 202211520 **Name:** RUDRESH RAJARAM RAUT

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	32 F	10		42 F			42	F	0	0
VLSI 620	Analog and Digital Communication	3	20 F	10		30 F			30	F	0	0
VLSI 633	Introduction to Micro-electromechanical Systems	3	26 F	11		37 F			37	F	0	0
VLSI 641	Elements of Radar System Engineering	3	62 P	15		77 P			77	B+	7	21
VLSI 650	Embedded Design Lab	1			18 P	18 P	17P		35	A	8	8
VLSI 660	Programming Lab II	1			14 P	14 P	19P		33	A	8	8
VLSI 670	FPGA Design Lab	2			40 P	40 P	14P		54	A	8	16
CV633	Air and Noise Pollution and Control	3	43 P	10		53 P			53	P	4	12
HM006	Cyber Law and IPR	3	53 P	14		67 P			67	B	6	18

Total Weighted Grade Points : 83

Entitlement Marks : # 10

Earned Credits: 13

SGPA : --

28 **Seat No:** 60825 **PR Number:** 202211695 **Name:** RUTAMBAR DILIP DESAI

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	40 P	11		51 P			51	P	4	12
VLSI 620	Analog and Digital Communication	3	42 P	12		54 P			54	P	4	12
VLSI 633	Introduction to Micro-electromechanical Systems	3	40 P	17		57 P			57	C	5	15
VLSI 641	Elements of Radar System Engineering	3	72 P	18		90 P			90	A	8	24
VLSI 650	Embedded Design Lab	1			20 P	20 P	18P		38	A+	9	9
VLSI 660	Programming Lab II	1			17 P	17 P	19P		36	A	8	8
VLSI 670	FPGA Design Lab	2			40 P	40 P	13P		53	A	8	16
CV633	Air and Noise Pollution and Control	3	40 P	19		59 P			59	C	5	15
HM006	Cyber Law and IPR	3	66 P	16		82 P			82	A	8	24

Total Weighted Grade Points : 135

Entitlement Marks : # 10

Earned Credits: 22

SGPA : 6.14



College Name: Government of Goa College of Engineering
Branch Name: Electronics (VLSI Design & Technology)
Examination : Semester VI May 2025

29 Seat No: 60826 **PR Number:** 202211393 **Name:** SAHIL LAXIMAN DHAWASKAR

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	64 P	12		76 P			76	B+	7	21
VLSI 620	Analog and Digital Communication	3	61 P	17		78 P			78	B+	7	21
VLSI 633	Introduction to Micro-electromechanical Systems	3	78 P	14		92 P			92	A	8	24
VLSI 641	Elements of Radar System Engineering	3	79 P	10		89 P			89	A	8	24
VLSI 650	Embedded Design Lab	1			24 P	24 P	20P		44	O	10	10
VLSI 660	Programming Lab II	1			19 P	19 P	19P		38	A+	9	9
VLSI 670	FPGA Design Lab	2			40 P	40 P	13P		53	A	8	16
CV633	Air and Noise Pollution and Control	3	57 P	18		75 P			75	B+	7	21
HM006	Cyber Law and IPR	3	68 P	19		87 P			87	A	8	24

Total Weighted Grade Points : 170

Entitlement Marks : # 10

Earned Credits: 22

SGPA : 7.73

30 Seat No: 60827 **PR Number:** 202211281 **Name:** SAMRUDHI SACHIN REGE

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	46 P	13		59 P			59	C	5	15
VLSI 620	Analog and Digital Communication	3	40 P	14		54 P			54	P	4	12
VLSI 633	Introduction to Micro-electromechanical Systems	3	45 P	15		60 P			60	C	5	15
VLSI 641	Elements of Radar System Engineering	3	65 P	19		84 P			84	A	8	24
VLSI 650	Embedded Design Lab	1			24 P	24 P	19P		43	O	10	10
VLSI 660	Programming Lab II	1			20 P	20 P	18P		38	A+	9	9
VLSI 670	FPGA Design Lab	2			46 P	46 P	20P		66	O	10	20
HM006	Cyber Law and IPR	3	54 P	15		69 P			69	B+	7	21
IT643	Computer Forensics and Cyber Security	3	66 P	14		80 P			80	B+	7	21

Total Weighted Grade Points : 147

Entitlement Marks : --

Earned Credits: 22

SGPA : 6.68

31 Seat No: 60828 **PR Number:** 202211639 **Name:** SHAYYUB NABISAB SHAIKH SANDHI

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	31 F	10		41 F			41	F	0	0
VLSI 620	Analog and Digital Communication	3	46 P	10		56 P			56	P	4	12
VLSI 633	Introduction to Micro-electromechanical Systems	3	28 F	13		41 F			41	F	0	0
VLSI 641	Elements of Radar System Engineering	3	75 P	15		90 P			90	A	8	24
VLSI 650	Embedded Design Lab	1			16 P	16 P	18P		34	A	8	8
VLSI 660	Programming Lab II	1			14 P	14 P	18P		32	B+	7	7
VLSI 670	FPGA Design Lab	2			38 P	38 P	11P		49	A	8	16
HM006	Cyber Law and IPR	3	60 P	18		78 P			78	B+	7	21
IT644	E Commerce	3	48 P	10		58 P			58	C	5	15

Total Weighted Grade Points : 103

Entitlement Marks : # 10

Earned Credits: 16

SGPA : --

32 Seat No: 60829 **PR Number:** 202211955 **Name:** SHERLEINE ALIZA GODINHO

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	40 P	10		50 P			50	P	4	12
VLSI 620	Analog and Digital Communication	3	19 F	10		29 F			29	F	0	0
VLSI 633	Introduction to Micro-electromechanical Systems	3	35 P#5	10		45 P#5			45 #5	P	4	12
VLSI 641	Elements of Radar System Engineering	3	55 P	15		70 P			70	B+	7	21
VLSI 650	Embedded Design Lab	1			16 P	16 P	10P		26	B	6	6
VLSI 660	Programming Lab II	1			11 P	11 P	10P		21	P	4	4
VLSI 670	FPGA Design Lab	2			25 P	25 P	5F		30	F	0	0
HM006	Cyber Law and IPR	3	60 P	8		68 P			68	B	6	18
IT643	Computer Forensics and Cyber Security	3	84 P	16		100 P			100	A+	9	27

Total Weighted Grade Points : 100

Entitlement Marks : # 10

Earned Credits: 17

SGPA : --

Result Register Report
Bachelor of Engineering(RC19-20) CBCS

College Name: Government of Goa College of Engineering
Branch Name: Electronics (VLSI Design & Technology)
Examination : Semester VI May 2025

33 Seat No: 60830 PR Number: 202309927 Name: SHIVANAND PRAKASH SASNOOR

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	51 P	11		62 P			62	C	5	15
VLSI 620	Analog and Digital Communication	3	40 P	10		50 P			50	P	4	12
VLSI 633	Introduction to Micro-electromechanical Systems	3	41 P	11		52 P			52	P	4	12
VLSI 641	Elements of Radar System Engineering	3	72 P	14		86 P			86	A	8	24
VLSI 650	Embedded Design Lab	1			17 P	17 P	16P		33	A	8	8
VLSI 660	Programming Lab II	1			17 P	17 P	15P		32	B+	7	7
VLSI 670	FPGA Design Lab	2			28 P	28 P	11P		39	B	6	12
HM006	Cyber Law and IPR	3	57 P	17		74 P			74	B+	7	21
ME644	Supply Chain Management	3	62 P	13		75 P			75	B+	7	21

Total Weighted Grade Points : 132

Entitlement Marks : --

Earned Credits: 22

SGPA : 6

34 Seat No: 60831 PR Number: 202211696 Name: SHIVDATT SUDAN SHENVI KUNCOLIENKAR

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	62 P	15		77 P			77	B+	7	21
VLSI 620	Analog and Digital Communication	3	62 P	12		74 P			74	B+	7	21
VLSI 633	Introduction to Micro-electromechanical Systems	3	70 P	10		80 P			80	B+	7	21
VLSI 641	Elements of Radar System Engineering	3	77 P	19		96 P			96	A+	9	27
VLSI 650	Embedded Design Lab	1			19 P	19 P	19P		38	A+	9	9
VLSI 660	Programming Lab II	1			15 P	15 P	20P		35	A	8	8
VLSI 670	FPGA Design Lab	2			40 P	40 P	20P		60	A+	9	18
HM006	Cyber Law and IPR	3	60 P	16		76 P			76	B+	7	21
IT643	Computer Forensics and Cyber Security	3	80 P	17		97 P			97	A+	9	27

Total Weighted Grade Points : 173

Entitlement Marks : --

Earned Credits: 22

SGPA : 7.86

35 Seat No: 60832 PR Number: 202211615 Name: SHREYA CHANDRAKANT KHOKARALKAR

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	32 F	12		44 F			44	F	0	0
VLSI 620	Analog and Digital Communication	3	40 P	14		54 P			54	P	4	12
VLSI 633	Introduction to Micro-electromechanical Systems	3	43 P	12		55 P			55	P	4	12
VLSI 641	Elements of Radar System Engineering	3	77 P	22		99 P			99	A+	9	27
VLSI 650	Embedded Design Lab	1			21 P	21 P	20P		41	A+	9	9
VLSI 660	Programming Lab II	1			18 P	18 P	19P		37	A	8	8
VLSI 670	FPGA Design Lab	2			40 P	40 P	12P		52	A	8	16
HM006	Cyber Law and IPR	3	57 P	11		68 P			68	B	6	18
IT643	Computer Forensics and Cyber Security	3	69 P	16		85 P			85	A	8	24

Total Weighted Grade Points : 126

Entitlement Marks : --

Earned Credits: 19

SGPA : --

36 Seat No: 60833 PR Number: 202211282 Name: SHREYASH PRAVIN P USGAONKAR

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	60 P	10		70 P			70	B+	7	21
VLSI 620	Analog and Digital Communication	3	54 P	11		65 P			65	B	6	18
VLSI 633	Introduction to Micro-electromechanical Systems	3	60 P	11		71 P			71	B+	7	21
VLSI 641	Elements of Radar System Engineering	3	87 P	12		99 P			99	A+	9	27
VLSI 650	Embedded Design Lab	1			15 P	15 P	19P		34	A	8	8
VLSI 660	Programming Lab II	1			16 P	16 P	17P		33	A	8	8
VLSI 670	FPGA Design Lab	2			40 P	40 P	18P		58	A+	9	18
CV633	Air and Noise Pollution and Control	3	69 P	6		75 P			75	B+	7	21
HM006	Cyber Law and IPR	3	67 P	10		77 P			77	B+	7	21

Total Weighted Grade Points : 163

Entitlement Marks : # 12

Earned Credits: 22

SGPA : 7.41

Result Register Report
Bachelor of Engineering(RC19-20) CBCS

College Name: Government of Goa College of Engineering

Branch Name: Electronics (VLSI Design & Technology)

Examination : Semester VI May 2025

37 Seat No: 60834 PR Number: 202211954 Name: SHVETALI ALIAS SAKSHI TANAJI THENGIL

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	54 P	17		71 P			71	B+	7	21
VLSI 620	Analog and Digital Communication	3	53 P	14		67 P			67	B	6	18
VLSI 633	Introduction to Micro-electromechanical Systems	3	54 P	15		69 P			69	B+	7	21
VLSI 641	Elements of Radar System Engineering	3	91 P	22		113 P			113	O	10	30
VLSI 650	Embedded Design Lab	1			23 P	23 P	20P		43	O	10	10
VLSI 660	Programming Lab II	1			20 P	20 P	22P		42	A+	9	9
VLSI 670	FPGA Design Lab	2			46 P	46 P	19P		65	O	10	20
HM006	Cyber Law and IPR	3	74 P	17		91 P			91	A	8	24
IT643	Computer Forensics and Cyber Security	3	76 P	17		93 P			93	A	8	24

Total Weighted Grade Points : 177

Entitlement Marks : --

Earned Credits: 22

SGPA : 8.05

38 Seat No: 60835 PR Number: 202211496 Name: SIDHANT VINOD PATEL

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	54 P	10		64 P			64	B	6	18
VLSI 620	Analog and Digital Communication	3	56 P	15		71 P			71	B+	7	21
VLSI 633	Introduction to Micro-electromechanical Systems	3	61 P	14		75 P			75	B+	7	21
VLSI 641	Elements of Radar System Engineering	3	69 P	17		86 P			86	A	8	24
VLSI 650	Embedded Design Lab	1			23 P	23 P	21P		44	O	10	10
VLSI 660	Programming Lab II	1			16 P	16 P	20P		36	A	8	8
VLSI 670	FPGA Design Lab	2			43 P	43 P	14P		57	A+	9	18
CV633	Air and Noise Pollution and Control	3	57 P	11		68 P			68	B	6	18
HM006	Cyber Law and IPR	3	67 P	20		87 P			87	A	8	24

Total Weighted Grade Points : 162

Entitlement Marks : # 10

Earned Credits: 22

SGPA : 7.36

39 Seat No: 60836 PR Number: 202311066 Name: SUGNESH NAGUESH NAIK

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	40 P	10		50 P			50	P	4	12
VLSI 620	Analog and Digital Communication	3	Ab	10		10 F			10	Ab	0	0
VLSI 633	Introduction to Micro-electromechanical Systems	3	40 P	10		50 P			50	P	4	12
VLSI 641	Elements of Radar System Engineering	3	58 P	14		72 P			72	B+	7	21
VLSI 650	Embedded Design Lab	1			16 P	16 P	10P		26	B	6	6
VLSI 660	Programming Lab II	1			15 P	15 P	18P		33	A	8	8
VLSI 670	FPGA Design Lab	2			25 P	25 P	5F		30	F	0	0
EE644	Electrical Energy Conservation & Auditing	3	48 P	10		58 P			58	C	5	15
HM006	Cyber Law and IPR	3	60 P	15		75 P			75	B+	7	21

Total Weighted Grade Points : 95

Entitlement Marks : --

Earned Credits: 17

SGPA : --

40 Seat No: 60837 PR Number: 202311067 Name: SUYASH NAIK

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	33 F	10		43 F			43	F	0	0
VLSI 620	Analog and Digital Communication	3	Ab	10		10 F			10	Ab	0	0
VLSI 633	Introduction to Micro-electromechanical Systems	3	32 F	11		43 F			43	F	0	0
VLSI 641	Elements of Radar System Engineering	3	63 P	16		79 P			79	B+	7	21
VLSI 650	Embedded Design Lab	1			16 P	16 P	16P		32	B+	7	7
VLSI 660	Programming Lab II	1			15 P	15 P	17P		32	B+	7	7
VLSI 670	FPGA Design Lab	2			25 P	25 P	5F		30	F	0	0
EE644	Electrical Energy Conservation & Auditing	3	41 P	10		51 P			51	P	4	12
HM006	Cyber Law and IPR	3	57 P	14		71 P			71	B+	7	21

Total Weighted Grade Points : 68

Entitlement Marks : --

Earned Credits: 11

SGPA : --



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Taleigao Plateau, Goa - 403206
Result Register Report
Bachelor of Engineering(RC19-20) CBCS

11 of 11

College Name: Government of Goa College of Engineering
Branch Name: Electronics (VLSI Design & Technology)
Examination : Semester VI May 2025

41 **Seat No:** 60838 **PR Number:** 202211497 **Name:** TEJAS SUDAN MAYEKAR

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	84 P	24		108 P			108	O	10	30
VLSI 620	Analog and Digital Communication	3	66 P	17		83 P			83	A	8	24
VLSI 633	Introduction to Micro-electromechanical Systems	3	77 P	22		99 P			99	A+	9	27
VLSI 641	Elements of Radar System Engineering	3	82 P	19		101 P			101	A+	9	27
VLSI 650	Embedded Design Lab	1			24 P	24 P	23P		47	O	10	10
VLSI 660	Programming Lab II	1			23 P	23 P	22P		45	O	10	10
VLSI 670	FPGA Design Lab	2			47 P	47 P	21P		68	O	10	20
CV633	Air and Noise Pollution and Control	3	66 P	22		88 P			88	A	8	24
HM006	Cyber Law and IPR	3	65 P	21		86 P			86	A	8	24

Total Weighted Grade Points : 196

Entitlement Marks : --

Earned Credits: 22

SGPA : 8.91

42 **Seat No:** 60839 **PR Number:** 202211413 **Name:** VAIDEHI GOURISH DABOLKAR

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	67 P	16		83 P			83	A	8	24
VLSI 620	Analog and Digital Communication	3	70 P	12		82 P			82	A	8	24
VLSI 633	Introduction to Micro-electromechanical Systems	3	71 P	11		82 P			82	A	8	24
VLSI 641	Elements of Radar System Engineering	3	90 P	22		112 P			112	O	10	30
VLSI 650	Embedded Design Lab	1			19 P	19 P	20P		39	A+	9	9
VLSI 660	Programming Lab II	1			12 P	12 P	19P		31	B+	7	7
VLSI 670	FPGA Design Lab	2			36 P	36 P	19P		55	A	8	16
HM006	Cyber Law and IPR	3	70 P	17		87 P			87	A	8	24
IT643	Computer Forensics and Cyber Security	3	73 P	13		86 P			86	A	8	24

Total Weighted Grade Points : 182

Entitlement Marks : # 12

Earned Credits: 22

SGPA : 8.27

43 **Seat No:** 60840 **PR Number:** 202211412 **Name:** VAISHNAVI KRISHNANAND NAIK

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	64 P	18		82 P			82	A	8	24
VLSI 620	Analog and Digital Communication	3	44 P	17		61 P			61	C	5	15
VLSI 633	Introduction to Micro-electromechanical Systems	3	55 P	14		69 P			69	B+	7	21
VLSI 641	Elements of Radar System Engineering	3	81 P	22		103 P			103	A+	9	27
VLSI 650	Embedded Design Lab	1			24 P	24 P	19P		43	O	10	10
VLSI 660	Programming Lab II	1			20 P	20 P	21P		41	A+	9	9
VLSI 670	FPGA Design Lab	2			46 P	46 P	19P		65	O	10	20
HM006	Cyber Law and IPR	3	70 P	14		84 P			84	A	8	24
IT643	Computer Forensics and Cyber Security	3	60 P	12		72 P			72	B+	7	21

Total Weighted Grade Points : 171

Entitlement Marks : --

Earned Credits: 22

SGPA : 7.77

44 **Seat No:** 60841 **PR Number:** 202211274 **Name:** VARAD RAJENDRA PEDNEKAR

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	57 P	16		73 P			73	B+	7	21
VLSI 620	Analog and Digital Communication	3	68 P	13		81 P			81	B+	7	21
VLSI 633	Introduction to Micro-electromechanical Systems	3	64 P	20		84 P			84	A	8	24
VLSI 641	Elements of Radar System Engineering	3	84 P	20		104 P			104	A+	9	27
VLSI 650	Embedded Design Lab	1			21 P	21 P	20P		41	A+	9	9
VLSI 660	Programming Lab II	1			20 P	20 P	18P		38	A+	9	9
VLSI 670	FPGA Design Lab	2			46 P	46 P	25P		71	O	10	20
HM006	Cyber Law and IPR	3	64 P	18		82 P			82	A	8	24
ME644	Supply Chain Management	3	90 P	22		112 P			112	O	10	30

Total Weighted Grade Points : 185

Entitlement Marks : --

Earned Credits: 22

SGPA : 8.41

Read By:

Checked By:

Date : **11 AUG 2025**

Asstt. Registrar Exam(Prof)
(Technical and Allied)

Asstt. Registrar Exam

Controller of Examinations