

Department of Electronics & Telecommunication Engineering

Goa College of Engineering,

Bhauasaheb Bhandodkar Technical Educational Complex

Farmagudi, Ponda Goa – 403401.



Dated: 23/12/2020

Time-Table: OneTime Online Internal Tests

Scheme: 2007-08

Semester	Course	Date	Time	Faculty in charge	Internal Test Syllabus
V	Control system engineering	29.12.2020	9.30am -11.30am	Prof. Virani (Prof. Milind)	Module -I
	Digital Signal processing	29.12.2020	2 pm – 4 pm	Prof. Nitesh	Module -I
	Probability theory & Random Processes	29.12.2020	9.30am -11.30am	Prof. Seby Mesquita	Module -III
VI	Antenna & wave Propagation	30.12.2020	9.30am -11.30am	Prof. Shruthi (Prof. Abhijit)	Module -I
	Peripheral Devices & Interfacing	30.12.2020	2 pm – 4 pm	Prof. Mangish	Module -I
	Communication Engineering-II	31.12.2020	9.30am -11.30am	Prof. Kutty	Module -I
	Electronic Instrumentation	31.12.2020	2 pm – 4 pm	Prof. Lohani	Module -III
VII	Data Communication	7.1.2021	9.30am -11.30am	Prof. Devendra	Module -I
	Microwave & Radar Engineering	7.1.2021	2 pm – 4 pm	Prof. Sangam	Module -IV
	Mobile Communication	2.1.2021	9.30am -11.30am	Prof. Amita	Module -I
	Optical Fiber Communication	2.1.2021	2 pm – 4 pm	Prof. Kutty	Module -I
	Embedded Systems	4.1.2021	9.30am -11.30am	Prof. Nayana	Module -I
	Process Control Instrumentation	4.1.2021	9.30am -11.30am	Prof. Lohani	Module -I

NOTE:

(1) On-line Test will be for internal marks specified in the respective scheme; (2) Question paper pattern - MCQ, Descriptive can be decided by the faculty. (3) Faculty members are to limit the portion for the test to any ONE unit only. (4) Only ONE on-line test will be conducted. (5) Head of Department shall review and approve the same

VIII	Satellite & Television Engineering	4.1.2021	2 pm – 4 pm	Prof. Virani	Unit - III
	Wireless Communication	5.1.2021	9.30am -11.30am	Prof. Amita	Module -II
	Microwave Networks & Applications	5.1.2021	2 pm – 4 pm	Prof. Sangam	Module -II
	Digital Image Processing	6.1.2021	9.30am -11.30am	Prof. Nitesh	Module -II

Scheme: 2016-17

Semester	Course	Date	Time	Faculty in charge	IT Syllabus
II	Engineering Graphics	30.12.2020	9.30am -11.30am	Prof. Gajesh Usgaonkar	Unit - I
III	Digital System Design	29.12.2020	9.30am -11.30am	Prof. Ameeta Amonkar	Unit - I
IV	Embedded Systems	29.12.2020	9.30am -11.30am	Prof. Nayana Shenvi	Unit - I

Scheme: 2019-20

Semester	Course	Date	Time	Faculty in charge	IT Syllabus
I	Basic Electrical & Electronics Engineering	2.1.2021	2 pm – 4 pm	Prof. Purti	Unit 3 (Diodes and Circuits: PN junction diode, V-I characteristics, Zener diode, breakdown mechanism in diodes, Diode Applications: Half-wave, Full-wave and Bridge Rectifiers, PIV; DC and r.m.s voltages, Ripple Factor. Voltage regulation using Zener diodes)

NOTE:

- (1) On-line Test will be for internal marks specified in the respective scheme;
- (2) Question paper pattern - MCQ, Descriptive can be decided by the faculty.
- (3) Faculty members are to limit the portion for the test to any ONE unit only.
- (4) Only ONE on-line test will be conducted.
- (5) Head of Department shall review and approve the same.

Dr. H. G. Virani
Professor & Head- ETC