

Electrical and Electronics Engineering Department
Goa College of Engineering,
Bhauasaheb Bandodkar Technical Educational Complex



Farmagudi, Ponda Goa – 403401.

Dated:23/12/2020

Time-Table:One Time Online Internal Tests

Scheme: 2007-08

Semester	Course	Date	Time	Faculty in charge	IT Syllabus
IV	Numerical Techniques and Programming	29-12-2020	9.30-11.30	Prof Ujwala	Module 1
	Linear Integrated Circuits	01-01-2021	9.30-11.30	Prof Suneeta	Module 1
	Electrical Power	01-01-2021	2.00-4.00	Prof Sidhi	Module 1
	Electrical machines II	02-01-2021	9.30-11.30	Prof Jayesh	Module 1
	Analog and Digital Communication	02-01-2021	2.00-4.00	Prof Ankita	Module 1
	Economic Management	04-01-2021	9.30-11.30	Prof Ashmita Kerkar	Module 1
V	Electromagnetic Theory	04-01-2021	2.00-4.00	Dr G R Kunkolienkar	Module 1
	Electrical Machines III	05-01-2021	9.30-11.30	Prof Nilesh	Module 1
	Design of Electronic Circuits	05-01-2021	2.00-4.00	Prof Sangeeta	Module 1
	Control Engineering	06-01-2021	9.30-11.30	Prof Sonali	Module 1

	Microprocessor & Interfacing	29-12-2020	2.00-4.00	Prof Suneeta	Module 1
	Electronic Instrumentation	06-01-2021	2.00-4.00	Prof Siddhant	Module 1
VI	Power Electronics	30-12-2020	9.30-11.30	Dr. Anant	Module 1
	Digital Signal Processing	30-12-2020	2.00-4.00	Prof Sonali	Module 1
	Electrical Power System I	07-01-2021	9.30-11.30	Prof Jayesh	Module 1
	Electrical Drives & Control	07-01-2021	2.00-4.00	Prof Aditi	Module 1
	Embedded Systems	08-01-2021	9.30-11.30	Prof Sangeeta	Module 1
	Electrical Machine Design	08-01-2021	2.00-4.00	Prof Noah	Module 1
VII	VLSI Circuit Design	31-01-2020	9.30-11.30	Prof Sangeeta	Module 1
	Electrical Power System II	31-01-2020	2.00-4.00	Prof Nilesh	Module 1
	Advanced Controlled Drives	09-01-2021	9.30-11.30	Prof Noah	Module 1
	Electrical Estimation & costing	09-01-2021	2.00-4.00	Prof Shradha	Module 1
	Flexible AC Transmission system	11-01-2021	9.30-11.30	Dr Anant	Module 1
	Neural Networks and Fuzzy logic	01-01-2021	2.00-4.00	Prof Suneeta	Module 1
VIII	High Voltage Engineering	29-12-2020	9.30-11.30	Dr G R Kunkolienkar	Module 1

NOTE:

- (1) On-line Test will be for internal marks specified in the respective scheme;(2) Question paper pattern - MCQ, Descriptive can be decided by the faculty.
- (3) Faculty members are to limit the portion for the test to any ONE unit only.(4) Only ONE on-line test will be conducted.
- (5) Head of Department shall review and approve the same.

Scheme: 2016-17

Semester	Course	Date	Time	Faculty in charge	IT Syllabus
III	Applied Mathematics	29-12-2020	9.30-11.30	Prof Ujwala	Unit 1
	Electronic Devices and Circuits	29-12-2020	2.00-4.00	Prof Ankita	Unit 1
	Electrical Machine - 1	30-12-2020	9.30-11.30	Prof Aditi	Unit 1
V	Power electronics	30-12-2020	2.00-4.00	Dr Anant	Unit 1
	control engineering	31-12-2020	9.30-11.30	Prof Sonali	Unit 1

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